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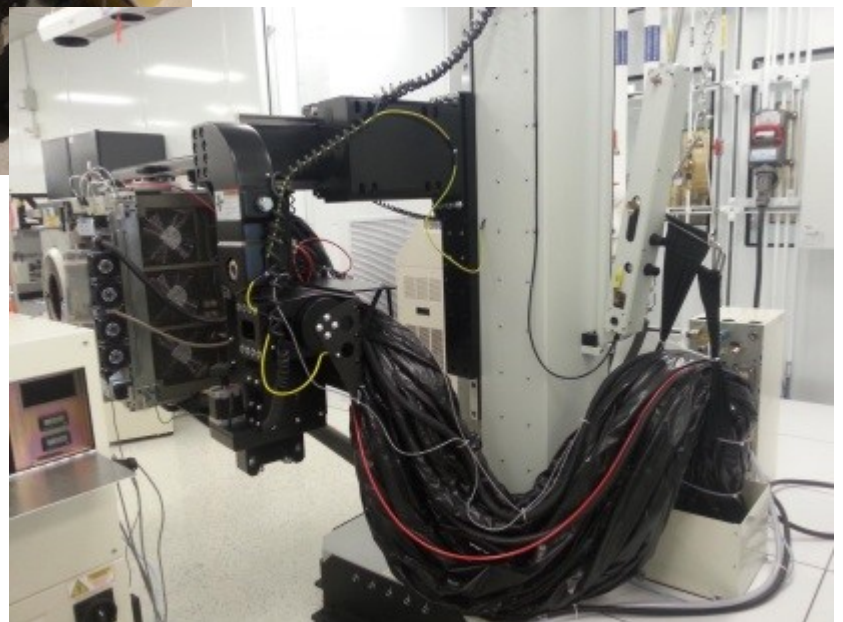
Advantest T6577 Test System (JAD-37242)

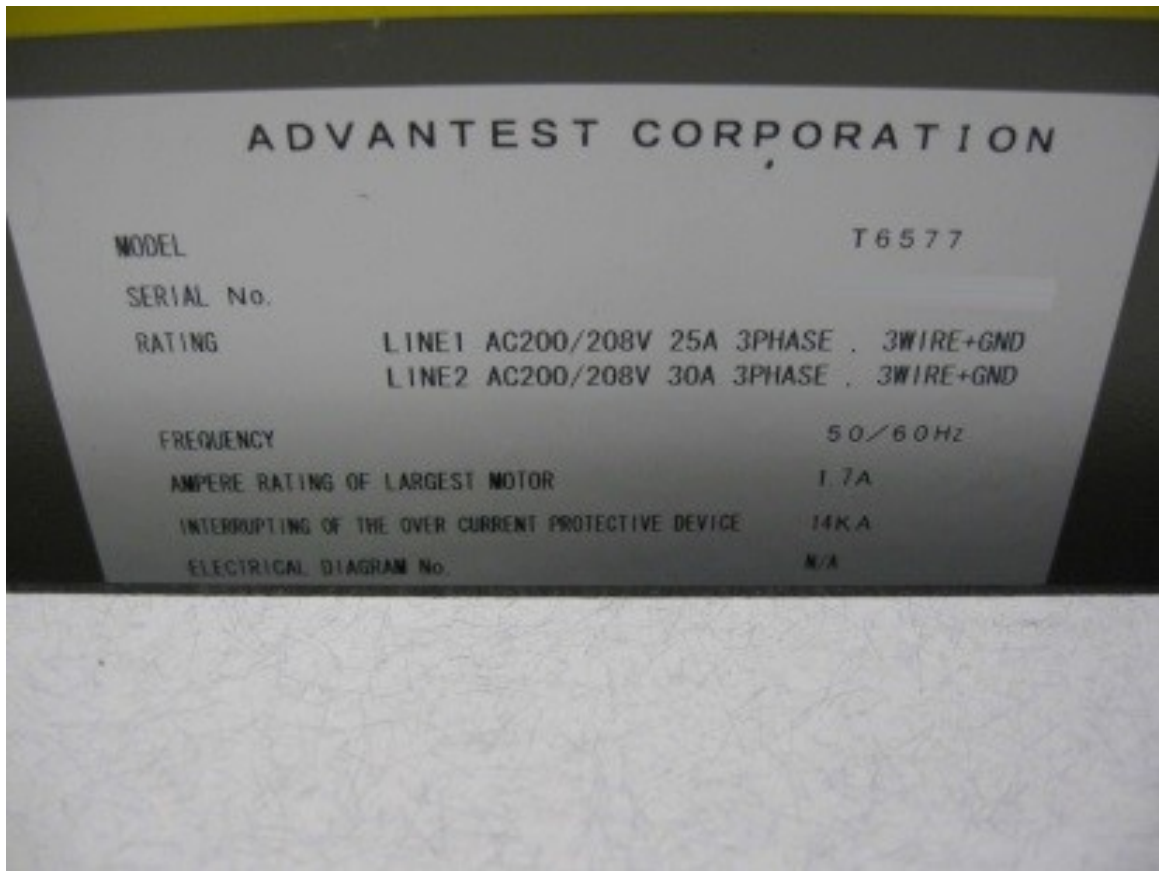
Make: Advantest
Model: T6577
Quantity: 1
Vintage: 2005
Inspection: Yes

Configuration:

Includes:
TC7 Controller
1024 Channels
STE Memory: 32 M
DPS 32 Channels @ 2A
DCAP (Data Capture Option)
ALPG / SCPG 16G
Address Fail Memory (AFM)
1024 Channels ZIF hi-fix/SH7-110014
1024 Channels ZIF diag PB/BIM-026739
1024 Channels ZIF cal PB/SH3-93882
512 Channels hi-fix/SH7-110012
512 Channels diag PB/BLS-026734X02
512 Channels cal PB/BLS-026219
OM2001 6577 RAM Manipulator
Configuration:
PIN Configuration
Mainframe: 1-1024
TH1 [CTD]: 1-1024
DPU Configuration:
DPS CH: 1-32
UDC CH: 1-8
MDC CH: 1-64
Line frequency: 60 Hz
DMM Model: R6581T

SQPG Configuration:
MAX VGC: 4 MW
MAX CTB: 64 MW
MAX TTB: 64 MW
Option configuration:
HRS Off
SCPG On
ALPG On
Mixed on
Option PG:
MAX SCPG: 16Gbit
MAX ALPG: 1 KW
MAX AFM: 1 MW
Mixed option: DCAP3
HI-FIX Type: 1024B





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Successfully loaded objects
Successfully removed tdl files
Successfully started file /tmp/advantest/_default/hardware.tdl
*****
* DIAGNOSIS VERSION      R1.08-1 (for viewpoint R8.09A or later)      *
* SYSTEM NAME           T6577  DIAGNOSIS                             *
* STARTED AT 2019/08/21 10:32:26                                     *
*****

----- SYSTEM CONFIGURATION INFORMATION -----
PIN CONFIGURATION
  MAINFRAME      :    1 - 1024
  TH1    [CTD    ] :    1 - 1024
DPU CONFIGURATION
  DPS  CH        :    1 - 32
  UDC  CH        :    1 - 8
  MDC  CH        :    1 - 64
  LINE FREQUENCY  : 60Hz
  DMM MODEL      : R6581T
SQPG CONFIGURATION
  MAX VGC        : 4MW
  MAX CTB        : 64MW
  MAX TTB        : 64MW
OPTIONS CONFIGURATIONS : HRS OFF  SCPG ON  ALPG ON  MIXED ON
OPTION PG
  MAX SCPG       : 16Gbit
  MAX ALPG       : 1KW
  MAX AFM        : 1MW
  MIXED OPTION   : DCAP3
HI-FIX TYPE      : 1024B

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----- DIAGNOSIS INFORMATION -----
DIAGNOSIS MODE      : NORMAL
DIAGNOSIS TEST NUMBER :    -1 -    -1
DIAGNOSIS SPEC.     : 100.0
DIAGNOSIS REPEAT COUNT : 1
DIAGNOSIS PIN INFORMATION
  MAINFRAME      :    1 - 1024
  TH1            :    1 - 1024
DIAGNOSIS DPU INFORMATION
  DPS  CH        :    1 - 32
  UDC  CH        :    1 - 8
  MDC  CH        :    1 - 64
DIAGNOSIS OPTIONS INFORMATION
  MIXED OPTION   : DCAP3

-----
TEST 140000 TH1 PB FLAG READ CHECK
TEST 140000 RESULT ----- 10:32:26 --> ***PASS
TEST 141000 TH1 PIN/CW/PCON PB CONNECTION CHECK
DPU      initialization start.
DPU      successfully initialized.
PMU      initialization start.
PMU      successfully initialized.
TEST 141000 RESULT ----- 10:41:43 --> ***PASS
TEST 101003 DPU      IFL  REG. W/R/SAVE CHECK
TEST 101003 RESULT ----- 10:41:43 --> ***PASS
TEST 101058 DPU      IFL  BUSY          CHECK
TEST 101058 RESULT ----- 10:41:43 --> ***PASS
TEST 101103 DPU      IFA  REG. W/R/SAVE CHECK
TEST 101103 RESULT ----- 10:41:43 --> ***PASS
TEST 101203 DPU  STN1  UDC_C REG. W/R/SAVE CHECK
TEST 101203 RESULT ----- 10:41:43 --> ***PASS
TEST 101603 DPU  STN1  DPS_C REG. W/R/SAVE CHECK
TEST 101603 RESULT ----- 10:41:43 --> ***PASS
TEST 101658 DPU  STN1  DPS_C ROM VS CH W/R CHECK

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TEST 101658 RESULT ----- 10:41:43 --> ***PASS
TEST 101663 DPU STN1 DPS_C PSRR REG. W/R CHECK
TEST 101663 RESULT ----- 10:41:44 --> ***PASS
TEST 101668 DPU STN1 DPS_C VS16 para W/R CHECK
TEST 101668 RESULT ----- 10:41:44 --> ***PASS
TEST 103328 DPU STN1 UDC VSIM VS ACCURACY CHECK
TEST 103328 RESULT ----- 10:41:51 --> ***PASS
TEST 103333 DPU STN1 UDC VSIM IM ACCURACY CHECK
TEST 103333 RESULT ----- 10:41:59 --> ***PASS
TEST 103338 DPU STN1 UDC VSIM CLAMP ACCURACY CHECK
TEST 103338 RESULT ----- 10:42:17 --> ***PASS
TEST 103343 DPU STN1 UDC ISVM IS ACCURACY CHECK
TEST 103343 RESULT ----- 10:42:56 --> ***PASS
TEST 103348 DPU STN1 UDC ISVM VM ACCURACY CHECK
TEST 103348 RESULT ----- 10:43:05 --> ***PASS
TEST 103353 DPU STN1 UDC ISVM CLAMP ACCURACY CHECK
TEST 103353 RESULT ----- 10:43:16 --> ***PASS
TEST 103358 DPU STN1 UDC MVM VM ACCURACY CHECK
TEST 103358 RESULT ----- 10:43:24 --> ***PASS
TEST 103728 DPU STN1 DPS VSIM VS ACCURACY CHECK
TEST 103728 RESULT ----- 10:43:36 --> ***PASS
TEST 103733 DPU STN1 DPS VSIM IM ACCURACY CHECK
TEST 103733 RESULT ----- 10:44:36 --> ***PASS
TEST 103738 DPU STN1 DPS VSIM CLAMP ACCURACY CHECK
TEST 103738 RESULT ----- 10:45:52 --> ***PASS
TEST 103763 DPU STN1 DPS VSVM VS ACCURACY CHECK
TEST 103763 RESULT ----- 10:46:01 --> ***PASS
TEST 103768 DPU STN1 DPS VSVM VM ACCURACY CHECK
TEST 103768 RESULT ----- 10:46:37 --> ***PASS
TEST 104028 DPU DPU-PG(SELF) CHECK
TEST 104028 RESULT ----- 10:46:37 --> ***PASS
TEST 104303 DPU STN1 UDC VSIM IM SETTLING CHECK
TEST 104303 RESULT ----- 10:47:23 --> ***PASS
TEST 104313 DPU STN1 UDC GUARD ALARM CHECK
TEST 104313 RESULT ----- 10:47:28 --> ***PASS
TEST 104343 DPU STN1 UDC PRECHECK
TEST 104343 RESULT ----- 10:47:29 --> ***PASS
TEST 104703 DPU STN1 DPS VSIM IM SETTLING CHECK
TEST 104703 RESULT ----- 10:48:57 --> ***PASS
TEST 104713 DPU STN1 DPS GUARD ALARM CHECK
TEST 104713 RESULT ----- 10:49:27 --> ***PASS
TEST 104718 DPU STN1 DPS OSC ALARM CHECK
TEST 104718 RESULT ----- 10:50:06 --> ***PASS
TEST 104723 DPU STN1 DPS HS_MODE BOARD REVISION CHECK
TEST 104723 RESULT ----- 10:50:06 --> ***PASS
TEST 104728 DPU STN1 DPS HS_MODE FUNCTION CHECK
TEST 104728 RESULT ----- 10:50:08 --> ***PASS
TEST 104733 DPU STN1 DPS HS_MODE SPIKE CHECK
TEST 104733 RESULT ----- 10:50:13 --> ***PASS
TEST 104743 DPU STN1 DPS PRECHECK
TEST 104743 RESULT ----- 10:50:18 --> ***PASS
TEST 106005 PMU MDC REG. WRITE/READ CHECK
TEST 106005 RESULT ----- 10:50:18 --> ***PASS
TEST 106010 PMU MDC BUSY CHECK
TEST 106010 RESULT ----- 10:50:18 --> ***PASS
TEST 106015 PMU MDC RESET BUSY/READ CHECK
TEST 106015 RESULT ----- 10:50:18 --> ***PASS
TEST 106020 PMU PPDC REG. WRITE/READ CHECK
TEST 106020 RESULT ----- 10:50:18 --> ***PASS
TEST 106025 PMU PPDC BUSY CHECK
TEST 106025 RESULT ----- 10:50:18 --> ***PASS
TEST 106030 PMU PPDC RESET BUSY/READ CHECK
TEST 106030 RESULT ----- 10:50:18 --> ***PASS
TEST 106515 PMU MDC VSIM CLAMP ACCURACY CHECK
TEST 106515 RESULT ----- 10:50:59 --> ***PASS

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TEST 106530	PMU	MDC	ISVM CLAMP	ACCURACY CHECK	
TEST 106530	RESULT	-----			10:51:26 --> ***PASS
TEST 106625	PMU	PPDC	VSIM VS	ACCURACY CHECK	
TEST 106625	RESULT	-----			10:51:28 --> ***PASS
TEST 106630	PMU	PPDC	VSIM IM	ACCURACY CHECK	
TEST 106630	RESULT	-----			10:51:30 --> ***PASS
TEST 106635	PMU	PPDC	VSIM CLAMP	ACCURACY CHECK	
TEST 106635	RESULT	-----			10:51:31 --> ***PASS
TEST 106640	PMU	PPDC	MVM VM	ACCURACY CHECK	
TEST 106640	RESULT	-----			10:51:33 --> ***PASS
TEST 106705	PMU	MDC	VSIM IM	SETTLING CHECK	
TEST 106705	RESULT	-----			10:51:34 --> ***PASS
TEST 106720	PMU	MDC	PRECHECK		
TEST 106720	RESULT	-----			10:51:36 --> ***PASS
TEST 106805	PMU	PPDC	VSIM IM	SETTLING CHECK	
TEST 106805	RESULT	-----			10:51:36 --> ***PASS
TEST 110100	SNC REGISTER READ/WRITE CHECK				
TEST 110100	RESULT	-----			10:51:36 --> ***PASS
TEST 110200	SNC REFCLK CHECK				
TEST 110200	RESULT	-----			10:51:45 --> ***PASS
TEST 110300	SNC REFCLK PAUSE CHECK				
TEST 110300	RESULT	-----			10:51:49 --> ***PASS
TEST 110400	SNC LOOP TPD MEASUREMENT CHECK				
TEST 110400	RESULT	-----			10:51:49 --> ***PASS
TEST 120000	SQPG PGIF REGISTER READ/WRITE CHECK				
TEST 120000	RESULT	-----			10:51:49 --> ***PASS
TEST 120010	SQPG SQPG REGISTER READ/WRITE CHECK				
TEST 120010	RESULT	-----			10:51:49 --> ***PASS
TEST 120020	SQPG TTB PIN MAP CHECK				
TEST 120020	RESULT	-----			10:52:23 --> ***PASS
TEST 120030	SQPG EVENT PIN MAP CHECK				
TEST 120030	RESULT	-----			10:52:23 --> ***PASS
TEST 120040	SQPG TTB/DFM REGISTER READ/WRITE CHECK BY PHYSICAL PIN				
TEST 120040	RESULT	-----			10:52:26 --> ***PASS
TEST 120050	SQPG TTB/DFM REGISTER READ/WRITE CHECK BY LOGICAL PIN				
TEST 120050	RESULT	-----			10:55:02 --> ***PASS
TEST 120060	SQPG EVENT REGISTER READ/WRITE CHECK BY PHYSICAL PIN				
TEST 120060	RESULT	-----			10:55:02 --> ***PASS
TEST 120070	SQPG EVENT REGISTER READ/WRITE CHECK BY LOGICAL PIN				
TEST 120070	RESULT	-----			10:55:02 --> ***PASS
TEST 120080	SQPG STE/DFM MEMORY SENSE CHECK				
TEST 120080	RESULT	-----			10:55:02 --> ***PASS
TEST 120100	SQPG RATE MEMORY READ/WRITE CHECK				
TEST 120100	RESULT	-----			10:55:02 --> ***PASS
TEST 120110	SQPG VGC MEMORY READ/WRITE CHECK				
TEST 120110	RESULT	-----			10:55:02 --> ***PASS
TEST 120120	SQPG CTB MEMORY READ/WRITE CHECK				
TEST 120120	RESULT	-----			10:55:02 --> ***PASS
TEST 120130	SQPG TTB MEMORY READ/WRITE CHECK				
TEST 120130	RESULT	-----			10:55:03 --> ***PASS
TEST 120140	SQPG EVENT MEMORY READ/WRITE CHECK				
TEST 120140	RESULT	-----			10:55:03 --> ***PASS
TEST 120150	SQPG UBM MEMORY READ/WRITE CHECK				
TEST 120150	RESULT	-----			10:55:16 --> ***PASS
TEST 120200	SQPG STE TRANSFER CHECK BY PHYSICAL PIN				
TEST 120200	RESULT	-----			10:57:40 --> ***PASS
TEST 120210	SQPG STE TRANSFER CHECK BY LOGICAL PIN				
TEST 120210	RESULT	-----			10:57:57 --> ***PASS
TEST 120220	SQPG EVENT TRANSFER CHECK BY PHYSICAL PIN				
TEST 120220	RESULT	-----			10:57:57 --> ***PASS
TEST 120230	SQPG EVENT TRANSFER CHECK BY LOGICAL PIN				
TEST 120230	RESULT	-----			10:57:57 --> ***PASS
TEST 120250	SQPG ERROR INSTRUCTION TRANS CHECK				
TEST 120250	RESULT	-----			10:57:57 --> ***PASS
TEST 120500	SQPG START CHECK				

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TEST 120500 RESULT ----- 10:57:59 --> ***PASS
TEST 120510 SQPG STOP CHECK
TEST 120510 RESULT ----- 10:57:59 --> ***PASS
TEST 120520 SQPG INTERRUPT CHECK
TEST 120520 RESULT ----- 10:57:59 --> ***PASS
TEST 120600 SQPG CTRL OUTPUT CHECK
TEST 120600 RESULT ----- 10:57:59 --> ***PASS
TEST 120610 SQPG CTB PATTERN OUTPUT CHECK
TEST 120610 RESULT ----- 10:57:59 --> ***PASS
TEST 120620 SQPG TTB PATTERN OUTPUT CHECK
TEST 120620 RESULT ----- 10:58:00 --> ***PASS
TEST 120630 SQPG EVENT PATTERN OUTPUT CHECK
TEST 120630 RESULT ----- 10:58:01 --> ***PASS
TEST 120650 SQPG TTB PATMUX PATTERN OUTPUT CHECK 1
TEST 120650 RESULT ----- 10:58:01 --> ***PASS
TEST 120660 SQPG TTB PATMUX PATTERN OUTPUT CHECK 2
TEST 120660 RESULT ----- 10:58:08 --> ***PASS
TEST 120670 SQPG EVENT PATMUX PATTERN OUTPUT CHECK 1
TEST 120670 RESULT ----- 10:58:08 --> ***PASS
TEST 120680 SQPG EVENT PATMUX PATTERN OUTPUT CHECK 2
TEST 120680 RESULT ----- 10:58:09 --> ***PASS
TEST 120700 SQPG CPE WINDOW SYNC CHECK
TEST 120700 RESULT ----- 10:58:09 --> ***PASS
TEST 121000 SQPG RATE PERIOD CHECK
TEST 121000 RESULT ----- 10:58:10 --> ***PASS
TEST 121100 SQPG REPEAT CHECK
TEST 121100 RESULT ----- 10:58:11 --> ***PASS
TEST 121110 SQPG CONTINUE MODE CHECK
TEST 121110 RESULT ----- 10:58:11 --> ***PASS
TEST 121150 SQPG SPA COMBINATION CHECK
TEST 121150 RESULT ----- 10:58:12 --> ***PASS
TEST 121200 SQPG PAUSE CHECK
TEST 121200 RESULT ----- 10:58:12 --> ***PASS
TEST 121210 SQPG PAUSE TIMER CHECK
TEST 121210 RESULT ----- 10:58:12 --> ***PASS
TEST 121300 SQPG OUT FIFO READ/WRITE CHECK
TEST 121300 RESULT ----- 10:58:14 --> ***PASS
TEST 121310 SQPG OUT HOLD CHECK
TEST 121310 RESULT ----- 10:58:15 --> ***PASS
TEST 121320 SQPG OUT ERROR MASK CHECK
TEST 121320 RESULT ----- 10:58:15 --> ***PASS
TEST 121330 SQPG OUT TIME OUT CHECK
TEST 121330 RESULT ----- 10:58:17 --> ***PASS
TEST 121500 SQPG JNI LOOP CHECK
TEST 121500 RESULT ----- 10:59:27 --> ***PASS
TEST 121510 SQPG JEC LOOP CHECK
TEST 121510 RESULT ----- 10:59:27 --> ***PASS
TEST 121800 SQPG VGC INDEX STACK ERROR CHECK
TEST 121800 RESULT ----- 10:59:27 --> ***PASS
TEST 121810 SQPG VGC CACHE ERROR CHECK
TEST 121810 RESULT ----- 10:59:27 --> ***PASS
TEST 121820 SQPG VGC REGISTER LOOP ERROR CHECK
TEST 121820 RESULT ----- 10:59:27 --> ***PASS
TEST 122000 SQPG START ADDRESS LOAD CHECK
TEST 122000 RESULT ----- 10:59:27 --> ***PASS
TEST 122010 SQPG START ADDRESS CHECK
TEST 122010 RESULT ----- 10:59:29 --> ***PASS
TEST 122100 SQPG PC BANK CONTROL CHECK 0
TEST 122100 RESULT ----- 10:59:30 --> ***PASS
TEST 122110 SQPG PC BANK CONTROL CHECK 1
TEST 122110 RESULT ----- 10:59:30 --> ***PASS
TEST 122120 SQPG PC BANK CONTROL CHECK 2
TEST 122120 RESULT ----- 10:59:30 --> ***PASS
TEST 122130 SQPG PC BANK CONTROL CHECK 3
TEST 122130 RESULT ----- 10:59:30 --> ***PASS

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TEST 122140 SQPG PC BANK CONTROL CHECK 4
TEST 122140 RESULT ----- 10:59:30 --> ***PASS
TEST 122150 SQPG PC BANK CONTROL CHECK 5
TEST 122150 RESULT ----- 10:59:30 --> ***PASS
TEST 122200 SQPG CTB READ BANK CHECK
TEST 122200 RESULT ----- 10:59:30 --> ***PASS
TEST 122210 SQPG TTB READ BANK CHECK
TEST 122210 RESULT ----- 10:59:33 --> ***PASS
TEST 122220 SQPG EVENT READ BANK CHECK
TEST 122220 RESULT ----- 10:59:33 --> ***PASS
TEST 122230 SQPG TTB PATMUX READ BANK CHECK
TEST 122230 RESULT ----- 10:59:38 --> ***PASS
TEST 122240 SQPG EVENT PATMUX READ BANK CHECK
TEST 122240 RESULT ----- 10:59:38 --> ***PASS
TEST 122300 SQPG CTB READ PC CHECK
TEST 122300 RESULT ----- 10:59:39 --> ***PASS
TEST 122310 SQPG TTB READ PC CHECK
TEST 122310 RESULT ----- 10:59:40 --> ***PASS
TEST 122320 SQPG EVENT READ PC CHECK
TEST 122320 RESULT ----- 10:59:40 --> ***PASS
TEST 122330 SQPG TTB PATMUX READ PC CHECK
TEST 122330 RESULT ----- 10:59:43 --> ***PASS
TEST 122340 SQPG EVENT PATMUX READ PC CHECK
TEST 122340 RESULT ----- 10:59:43 --> ***PASS
TEST 122350 SQPG TTB RBANK CHECK 2
TEST 122350 RESULT ----- 10:59:43 --> ***PASS
TEST 122360 SQPG EVENT RBANK CHECK 2
TEST 122360 RESULT ----- 10:59:44 --> ***PASS
TEST 122800 SQPG CTB PARITY GENERATOR & PARITY ERROR CHECK
TEST 122800 RESULT ----- 10:59:44 --> ***PASS
TEST 122810 SQPG TTB PARITY GENERATOR & PARITY ERROR CHECK
TEST 122810 RESULT ----- 10:59:58 --> ***PASS
TEST 122820 SQPG EVENT PARITY GENERATOR & PARITY ERROR CHECK
TEST 122820 RESULT ----- 10:59:59 --> ***PASS
TEST 124000 SQPG DFM MODE CHECK 1
TEST 124000 RESULT ----- 10:59:59 --> ***PASS
TEST 124010 SQPG DFM MODE CHECK 2
TEST 124010 RESULT ----- 10:59:59 --> ***PASS
TEST 124020 SQPG DFM LOOP COUNT CHECK
TEST 124020 RESULT ----- 10:59:59 --> ***PASS
TEST 124030 SQPG DFM TTB CONTROL SIG CHECK
TEST 124030 RESULT ----- 10:59:59 --> ***PASS
TEST 125000 SQPG TTB PARALLEL REGISTER READ/WRITE CHECK
TEST 125000 RESULT ----- 11:00:11 --> ***PASS
TEST 125002 SQPG TTB PARALLEL REGISTER READ/WRITE CHECK 2
TEST 125002 RESULT ----- 11:00:26 --> ***PASS
TEST 125010 SQPG TTB PARALLEL TRANSFER CHECK
TEST 125010 RESULT ----- 11:00:49 --> ***PASS
TEST 125012 SQPG TTB PARALLEL TRANSFER CHECK 2
TEST 125012 RESULT ----- 11:01:15 --> ***PASS
TEST 125100 SQPG TTB PARTIAL DUT REGISTER READ/WRITE CHECK
TEST 125100 RESULT ----- 11:01:29 --> ***PASS
TEST 125200 SQPG CTB MOVE CHECK
TEST 125200 RESULT ----- 11:02:40 --> ***PASS
TEST 125210 SQPG TTB MOVE CHECK
TEST 125210 RESULT ----- 11:03:54 --> ***PASS
TEST 125220 SQPG CTB MEM INIT CHECK
TEST 125220 RESULT ----- 11:03:54 --> ***PASS
TEST 125230 SQPG TTB MEM INIT CHECK
TEST 125230 RESULT ----- 11:03:55 --> ***PASS
TEST 125300 SQPG VGC MEMORY MARCHING CHECK
TEST 125300 RESULT ----- 11:03:56 --> ***PASS
TEST 125310 SQPG CTB MEMORY MARCHING CHECK
TEST 125310 RESULT ----- 11:04:12 --> ***PASS
TEST 125320 SQPG TTB MEMORY MARCHING CHECK

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TEST 125320 RESULT ----- 11:05:00 --> ***PASS
TEST 125330 SQPG EVENT TTB MEMORY MARCHING CHECK
TEST 125330 RESULT ----- 11:05:45 --> ***PASS
TEST 125400 SQPG FREE RUN CHECK
TEST 125400 RESULT ----- 11:05:45 --> ***PASS
TEST 125410 SQPG HRESO CHECK
TEST 125410 RESULT ----- 11:05:45 --> ***PASS
TEST 130000 FPU REGISTER READ/WRITE 1 CHECK (FPIF)
TEST 130000 RESULT ----- 11:09:07 --> ***PASS
TEST 130100 FPU REGISTER READ/WRITE 2 CHECK (FP)
TEST 130100 RESULT ----- 11:12:12 --> ***PASS
TEST 130200 FPU REGISTER READ/WRITE 3 CHECK (PARALLEL)
TEST 130200 RESULT ----- 11:13:11 --> ***PASS
TEST 130300 FPU COUNTER CHECK
TEST 130300 RESULT ----- 11:13:29 --> ***PASS
TEST 130350 FPU TEMPERATURE UNIT DAC ACTION CHECK
FPU      initialization start.
FPU      successfully initialized.
TEST 130350 RESULT ----- 11:14:04 --> ***PASS
TEST 137000 TRG REGISTER READ/WRITE CHECK
TEST 137000 RESULT ----- 11:14:10 --> ***PASS
TEST 137300 TRG COUNTER CHECK
TEST 137300 RESULT ----- 11:14:16 --> ***PASS
TEST 130400 FPU/TRG INITIALIZE CHECK
TEST 130400 RESULT ----- 11:14:48 --> ***PASS
TEST 130500 FPU INPUT CHECK
TEST 130500 RESULT ----- 11:14:54 --> ***PASS
TEST 130600 FPU LINEARITY CHECK (PTG)
FPU      initialization start.
FPU      successfully initialized.
TEST 130600 RESULT ----- 11:21:13 --> ***PASS
TEST 130700 FPU MUTUAL COMPARE CHECK
TEST 130700 RESULT ----- 11:21:41 --> ***PASS
TEST 130800 FPU FORMAT CONTROL CHECK
FPU      initialization start.
FPU      successfully initialized.
TEST 130800 RESULT ----- 11:22:38 --> ***PASS
TEST 130900 FPU DIGITAL COMPARE CHECK
FPU      initialization start.
FPU      successfully initialized.
TEST 130900 RESULT ----- 11:23:36 --> ***PASS
TEST 137500 TRG INPUT CHECK
SNC      initialization start.
SNC      successfully initialized.
TEST 137500 RESULT ----- 11:23:46 --> ***PASS
TEST 137600 TRG LINEARITY CHECK (PTG)
TRIGFP   initialization start.
TRIGFP   successfully initialized.
TEST 137600 RESULT ----- 11:24:21 --> ***PASS
TEST 137700 TRG MUTUAL COMPARE CHECK
TEST 137700 RESULT ----- 11:24:23 --> ***PASS
TEST 137800 TRG FORMAT CONTROL CHECK
TRIGFP   initialization start.
TRIGFP   successfully initialized.
TEST 137800 RESULT ----- 11:24:37 --> ***PASS
TEST 137900 TRG DIGITAL COMPARE CHECK
TRIGFP   initialization start.
TRIGFP   successfully initialized.
TEST 137900 RESULT ----- 11:24:49 --> ***PASS
TEST 140100 TH1 STATION SELECT REGISTER RESET CHECK
TEST 140100 RESULT ----- 11:24:49 --> ***PASS
TEST 140105 TH1 STATION SELECT REGISTER SYSTEM RESET CHECK
TEST 140105 RESULT ----- 11:24:49 --> ***PASS
TEST 140110 TH1 PIN DOMAIN REGISTER RESET CHECK
TEST 140110 RESULT ----- 11:24:50 --> ***PASS

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TEST 140115 TH1 PIN DOMAIN REGISTER SYSTEM RESET CHECK
TEST 140115 RESULT ----- 11:24:51 --> ***PASS
TEST 140120 TH1 CONT REGISTER RESET CHECK
TEST 140120 RESULT ----- 11:24:51 --> ***PASS
TEST 140125 TH1 CONT REGISTER SYSTEM RESET CHECK
TEST 140125 RESULT ----- 11:24:51 --> ***PASS
TEST 140150 TH1 DC PARA RESET CHECK
TEST 140150 RESULT ----- 11:25:10 --> ***PASS
TEST 140200 TH1 RON/ROF REGISTER READ/WRITE CHECK
TEST 140200 RESULT ----- 11:25:10 --> ***PASS
TEST 140300 TH1 PIN/CONT REGISTER READ/WRITE CHECK
TEST 140300 RESULT ----- 11:25:13 --> ***PASS
TEST 140310 TH1 OFFSET & GAIN REGISTER READ/WRITE CHECK
TEST 140310 RESULT ----- 11:25:14 --> ***PASS
TEST 140500 TH1 RON/ROFF TIMER OPERATION TIME CHECK
TEST 140500 RESULT ----- 11:25:23 --> ***PASS
TEST 140510 TH1 RON/ROFF TIMER OPERATION RON/ROFF SET CHECK
TEST 140510 RESULT ----- 11:25:23 --> ***PASS
TEST 140520 TH1 DIRECT RON OPERATION RON/ROFF SET CHECK
TEST 140520 RESULT ----- 11:25:23 --> ***PASS
TEST 140530 TH1 SETTLING BUSY FLAG OPERATION CHECK
TEST 140530 RESULT ----- 11:25:34 --> ***PASS
TEST 140540 TH1 SETTLING BUSY FLAG OPERATION2 CHECK
TEST 140540 RESULT ----- 11:25:53 --> ***PASS
TEST 140600 TH1 PIN GROUP SETUP OPERATION CHECK
TEST 140600 RESULT ----- 11:25:55 --> ***PASS
TEST 140610 TH1 PIN PARALLEL MODE READ/WRITE CHECK
TEST 140610 RESULT ----- 11:27:49 --> ***PASS
TEST 140700 TH1 TEMP CHECK
TEST 140700 RESULT ----- 11:27:51 --> ***PASS
TEST 140900 TH1 INIT TH ADJUSTMENT CHECK
SNC      initialization start.
SNC      successfully initialized.
TEST 140900 RESULT ----- 11:36:08 --> ***PASS
TEST 141100 TH1 DR OUTPUT VOLTAGE LINEARITY & RON/ROF CHECK
TEST 141100 RESULT ----- 11:36:47 --> ***PASS
TEST 141110 TH1 DR OUTPUT CURRENT & IMPEDANCE CHECK
TEST 141110 RESULT ----- 11:37:07 --> ***PASS
TEST 141120 TH1 DR OUTPUT CURRENT LIMIT CHECK
TEST 141120 RESULT ----- 11:37:16 --> ***PASS
TEST 141200 TH1 VT LINEARITY VOLTAGE & RON/ROF CHECK
TEST 141200 RESULT ----- 11:37:37 --> ***PASS
TEST 141210 TH1 VT CURRENT & RESISTANCE CHECK
TEST 141210 RESULT ----- 11:38:08 --> ***PASS
TEST 141300 TH1 PL CURRENT LINEARITY VOLTAGE & RON/ROF CHECK
TEST 141300 RESULT ----- 11:39:07 --> ***PASS
TEST 141310 TH1 PL THRESHOLD VOLTAGE CHECK
TEST 141310 RESULT ----- 11:39:16 --> ***PASS
TEST 141320 TH1 PL VT VOLTAGE CHECK
TEST 141320 RESULT ----- 11:39:57 --> ***PASS
TEST 141400 TH1 LIMITER FAIL SENSE VOLTAGE CHECK
TEST 141400 RESULT ----- 11:43:58 --> ***PASS
TEST 141500 TH1 LIMITER FAIL MASK OPERATION CHECK
TEST 141500 RESULT ----- 11:44:23 --> ***PASS
TEST 141600 TH1 TH1 RELAY OFF LEAK CURRENT CHECK
TEST 141600 RESULT ----- 11:44:40 --> ***PASS
TEST 142000 TH1 I/O LEAK CURRENT CHECK
TEST 142000 RESULT ----- 11:44:57 --> ***PASS
TEST 142100 TH1 CP(HiZ) REFERENCE VOLTAGE LINEARITY CHECK
TEST 142100 RESULT ----- 11:50:35 --> ***PASS
TEST 142110 TH1 CP(VT) REFERENCE VOLTAGE LINEARITY CHECK
TEST 142110 RESULT ----- 11:59:01 --> ***PASS
TEST 142200 TH1 DCLMP VOLTAGE LINEARITY & RON/ROF CHECK
TEST 142200 RESULT ----- 11:59:42 --> ***PASS
TEST 142210 TH1 DCLMP CURRENT LIMIT CHECK

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TEST 142210 RESULT ----- 11:59:51 --> ***PASS
TEST 144000 TH1 PRE DR - CP PHASE CHECK
TEST 144000 RESULT ----- 11:59:59 --> ***PASS
TEST 144100 TH1 PRE I/O TIMING (NORMAL MODE) CHECK
TEST 144100 RESULT ----- 12:00:20 --> ***PASS
TEST 144200 TH1 PRE DRE MINIMUM ON/OFF (NORMAL MODE) CHECK
TEST 144200 RESULT ----- 12:00:54 --> ***PASS
TEST 144500 TH1 DRIVER/COMPARETER TR/TF CHECK
TEST 144500 RESULT ----- 12:02:11 --> ***PASS
TEST 145000 TH1 DR - CP PHASE CHECK
TEST 145000 RESULT ----- 12:02:20 --> ***PASS
TEST 145100 TH1 I/O TIMING (NORMAL MODE) CHECK
TEST 145100 RESULT ----- 12:02:40 --> ***PASS
TEST 145120 TH1 I/O TIMING (VTT MODE) CHECK
TEST 145120 RESULT ----- 12:03:01 --> ***PASS
TEST 145200 TH1 DRE MINIMUM ON/OFF (NORMAL MODE) CHECK
TEST 145200 RESULT ----- 12:03:35 --> ***PASS
TEST 145220 TH1 DRE MINIMUM ON/OFF (VTT MODE) CHECK
TEST 145220 RESULT ----- 12:04:09 --> ***PASS
TEST 140910 TH1 INIT TRIGTH ADJUSTMENT CHECK
TRIGFP      initialization start.
TRIGFP      successfully initialized.
TEST 140910 RESULT ----- 12:04:33 --> ***PASS
TEST 143000 TH1 TRIGGER DR OUTPUT VOLTAGE LINEARITY CHECK
TEST 143000 RESULT ----- 12:04:39 --> ***PASS
TEST 143010 TH1 TRIGGER DR OUTPUT CURRENT & IMPEDANCE CHECK
TEST 143010 RESULT ----- 12:04:46 --> ***PASS
TEST 143100 TH1 TRIGGER CP(HiZ) REFERENCE VOLTAGE LINEARITY CHECK
TEST 143100 RESULT ----- 12:04:56 --> ***PASS
TEST 143105 TH1 TRIGGER CP(VT) REFERENCE VOLTAGE LINEARITY CHECK
TEST 143105 RESULT ----- 12:05:12 --> ***PASS
TEST 143110 TH1 TRIGGER CP(HiZ) REFERENCE VOLTAGE HYSTERESIS CHECK
TEST 143110 RESULT ----- 12:05:20 --> ***PASS
TEST 143115 TH1 TRIGGER CP(VT) REFERENCE VOLTAGE HYSTERESIS CHECK
TEST 143115 RESULT ----- 12:06:06 --> ***PASS
TEST 150000 FPU/TRG AFTER TH1 INITIALIZE CHECK
TEST 150000 RESULT ----- 12:06:27 --> ***PASS
TEST 150100 FPU AFTER TH1 FUNCTION CHECK
TEST 150100 RESULT ----- 12:07:53 --> ***PASS
TEST 157100 TRG AFTER TH1 FUNCTION CHECK
FPU          initialization start.
FPU          successfully initialized.
TRIGFP      initialization start.
TRIGFP      successfully initialized.
TEST 157100 RESULT ----- 12:08:30 --> ***PASS
TEST 160000 SQPG DFM FAIL PATH CHECK
TEST 160000 RESULT ----- 12:08:32 --> ***PASS
TEST 160010 SQPG DFM TOFL PATH CHECK
TEST 160010 RESULT ----- 12:08:32 --> ***PASS
TEST 160020 SQPG DFM STORE PATH CHECK
TEST 160020 RESULT ----- 12:08:33 --> ***PASS
TEST 160030 SQPG DFM FULL STORE CHECK
TEST 160030 RESULT ----- 12:08:34 --> ***PASS
TEST 160500 SQPG HMATCH FLAGH PATH CHECK
TEST 160500 RESULT ----- 12:08:35 --> ***PASS
TEST 160600 SQPG LMATCH FLAGL PATH CHECK
TEST 160600 RESULT ----- 12:08:35 --> ***PASS
TEST 161000 SQPG DFM BOUNDARY CHECK 1
TEST 161000 RESULT ----- 12:08:36 --> ***PASS
TEST 161010 SQPG DFM BOUNDARY CHECK 2
TEST 161010 RESULT ----- 12:08:36 --> ***PASS
TEST 161020 SQPG DFM BOUNDARY CHECK 3
TEST 161020 RESULT ----- 12:08:36 --> ***PASS
TEST 161030 SQPG DFM DONE FLAG MODE CHECK
TEST 161030 RESULT ----- 12:08:36 --> ***PASS

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TEST 161100	SQPG DFM FAIL STORE CHECK 1		
TEST 161100	RESULT -----	12:08:36 -->	***PASS
TEST 161110	SQPG DFM FAIL STORE CHECK 2		
TEST 161110	RESULT -----	12:08:37 -->	***PASS
TEST 161200	SQPG DFM NAVIGATION CHECK 1		
TEST 161200	RESULT -----	12:08:37 -->	***PASS
TEST 161210	SQPG DFM NAVIGATION CHECK 2		
TEST 161210	RESULT -----	12:08:38 -->	***PASS
TEST 161500	SQPG DFM TOFL PARALLEL CHECK		
TEST 161500	RESULT -----	12:08:38 -->	***PASS
TEST 161502	SQPG DFM TOFL PARALLEL CHECK 2		
TEST 161502	RESULT -----	12:08:38 -->	***PASS
TEST 161510	SQPG DFM TOFL PARALLEL AND FAIL CHECK		
TEST 161510	RESULT -----	12:08:39 -->	***PASS
TEST 161520	SQPG DFM TOFL PARALLEL OR FAIL CHECK		
TEST 161520	RESULT -----	12:08:39 -->	***PASS
TEST 161530	SQPG DFM TOFL PARALLEL FIRST FAIL CHECK		
TEST 161530	RESULT -----	12:08:39 -->	***PASS
TEST 162000	SQPG HMATCH FUNCTION CHECK 1		
TEST 162000	RESULT -----	12:08:40 -->	***PASS
TEST 162010	SQPG HMATCH FUNCTION CHECK 2		
TEST 162010	RESULT -----	12:08:40 -->	***PASS
TEST 162020	SQPG HMATCH PULSE TRAIN CHECK 1		
TEST 162020	RESULT -----	12:08:45 -->	***PASS
TEST 162021	SQPG HMATCH PULSE TRAIN CHECK 2		
TEST 162021	RESULT -----	12:08:51 -->	***PASS
TEST 162100	SQPG LMATCH FUNCTION CHECK 1		
TEST 162100	RESULT -----	12:08:51 -->	***PASS
TEST 162110	SQPG LMATCH FUNCTION CHECK 2		
TEST 162110	RESULT -----	12:08:52 -->	***PASS
TEST 162120	SQPG LMATCH FUNCTION CHECK 3		
TEST 162120	RESULT -----	12:08:52 -->	***PASS
TEST 162500	SQPG MATCH DUT REJECT CHECK 1		
TEST 162500	RESULT -----	12:08:52 -->	***PASS
TEST 162510	SQPG MATCH DUT REJECT CHECK 2		
TEST 162510	RESULT -----	12:08:53 -->	***PASS
TEST 162520	SQPG MATCH DUT REJECT CHECK 3		
TEST 162520	RESULT -----	12:08:53 -->	***PASS
TEST 162530	SQPG MATCH DUT REJECT CHECK 4		
TEST 162530	RESULT -----	12:08:54 -->	***PASS
TEST 162800	SQPG MATCH RATE ERROR CHECK		
TEST 162800	RESULT -----	12:08:54 -->	***PASS
TEST 165100	SQPG DFM PARTIAL DUT READ CHECK		
TEST 165100	RESULT -----	12:08:56 -->	***PASS
TEST 165102	SQPG DFM PARTIAL DUT READ CHECK 2		
TEST 165102	RESULT -----	12:08:59 -->	***PASS
TEST 166100	SQPG WAIT OR FAIL CHECK		
TEST 166100	RESULT -----	12:09:00 -->	***PASS
TEST 170000	SCPG REGISTER READ/WRITE CHECK		
TEST 170000	RESULT -----	12:09:02 -->	***PASS
TEST 170100	SCPG MEMORY READ/WRITE CHECK		
TEST 170100	RESULT -----	12:09:02 -->	***PASS
TEST 170110	SCPG DAW MEMORY READ/WRITE CHECK		
TEST 170110	RESULT -----	12:09:02 -->	***PASS
TEST 170250	SCPG VECTOR FILE GROUP CHECK		
TEST 170250	RESULT -----	12:09:02 -->	***PASS
TEST 170500	SCPG PATTERN OUTPUT CHECK		
TEST 170500	RESULT -----	12:09:02 -->	***PASS
TEST 171000	SCPG START ADDRESS LOAD CHECK		
TEST 171000	RESULT -----	12:09:03 -->	***PASS
TEST 171100	SCPG PC BANK CONTROL CHECK 0		
TEST 171100	RESULT -----	12:09:03 -->	***PASS
TEST 171110	SCPG PC BANK CONTROL CHECK 1		
TEST 171110	RESULT -----	12:09:03 -->	***PASS
TEST 171160	SCPG PC BANK TEST LINE CHECK		

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TEST 171160 RESULT ----- 12:09:19 --> ***PASS
TEST 171170 SCPG SCINC SCOUT CONTROL CHECK
TEST 171170 RESULT ----- 12:09:19 --> ***PASS
TEST 171200 SCPG READ BANK CHECK
TEST 171200 RESULT ----- 12:09:20 --> ***PASS
TEST 171300 SCPG READ PC CHECK
TEST 171300 RESULT ----- 12:09:59 --> ***PASS
TEST 171400 SCPG DAW LOOP CHECK 1
TEST 171400 RESULT ----- 12:09:59 --> ***PASS
TEST 171410 SCPG DAW LOOP CHECK 2
TEST 171410 RESULT ----- 12:09:59 --> ***PASS
TEST 171420 SCPG DAW LOOP CHECK 3
TEST 171420 RESULT ----- 12:09:59 --> ***PASS
TEST 171430 SCPG DAW LOOP CHECK 4
TEST 171430 RESULT ----- 12:09:59 --> ***PASS
TEST 171440 SCPG DAW LOOP CHECK 5
TEST 171440 RESULT ----- 12:09:59 --> ***PASS
TEST 171550 SCPG DAWEND-DREQ/JUMP CHECK
TEST 171550 RESULT ----- 12:11:28 --> ***PASS
TEST 171800 SCPG PARITY GENERATOR & PARITY ERROR CHECK
TEST 171800 RESULT ----- 12:11:28 --> ***PASS
TEST 171810 SCPG PARITY ERROR CHECK2
TEST 171810 RESULT ----- 12:11:29 --> ***PASS
TEST 171900 SCPG MEMORY MARCHING CHECK
TEST 171900 RESULT ----- 12:13:27 --> ***PASS
TEST 172001 ALPG REGISTER R/W CHECK
TEST 172001 RESULT ----- 12:13:28 --> ***PASS
TEST 172002 ALPG WCS R/W CHECK
TEST 172002 RESULT ----- 12:13:28 --> ***PASS
TEST 172006 ALPG UNIT SEL FUNCTION CHECK
TEST 172006 RESULT ----- 12:13:28 --> ***PASS
TEST 172007 ALPG ALPG MEMORY SELECT CHECK
TEST 172007 RESULT ----- 12:13:28 --> ***PASS
TEST 172011 ALPG WCS1-10 R/W CHECK (MARCHING)
TEST 172011 RESULT ----- 12:13:29 --> ***PASS
TEST 172014 ALPG XSCRAM R/W CHECK (MARCHING)
TEST 172014 RESULT ----- 12:13:30 --> ***PASS
TEST 172015 ALPG YSCRAM R/W CHECK (MARCHING)
TEST 172015 RESULT ----- 12:13:31 --> ***PASS
TEST 172016 ALPG ARIRAM R/W CHECK (MARCHING)
TEST 172016 RESULT ----- 12:13:31 --> ***PASS
TEST 172020 ALPG TBRST (TESTER BUS RESET) CHECK
TEST 172020 RESULT ----- 12:13:31 --> ***PASS
TEST 172021 ALPG PG RESET (CONTROL REG.) CHECK
TEST 172021 RESULT ----- 12:13:31 --> ***PASS
TEST 172031 ALPG WCS11 R/W CHECK
TEST 172031 RESULT ----- 12:13:31 --> ***PASS
TEST 172200 ALPG AGEN SET INSTRUCTION CHECK 1
TEST 172200 RESULT ----- 12:13:31 --> ***PASS
TEST 172201 ALPG AGEN SET INSTRUCTION CHECK 2
TEST 172201 RESULT ----- 12:13:31 --> ***PASS
TEST 172202 ALPG AGEN XT FUNCTION CHECK
TEST 172202 RESULT ----- 12:13:31 --> ***PASS
TEST 172203 ALPG AGEN YT FUNCTION CHECK
TEST 172203 RESULT ----- 12:13:31 --> ***PASS
TEST 172204 ALPG AGEN XB INSTRUCTION CHECK
TEST 172204 RESULT ----- 12:13:31 --> ***PASS
TEST 172205 ALPG AGEN YB INSTRUCTION CHECK
TEST 172205 RESULT ----- 12:13:31 --> ***PASS
TEST 172210 ALPG AGEN D3 INSTRUCTION CHECK
TEST 172210 RESULT ----- 12:13:31 --> ***PASS
TEST 172211 ALPG AGEN D4 INSTRUCTION CHECK
TEST 172211 RESULT ----- 12:13:31 --> ***PASS
TEST 172212 ALPG AGEN D3 D4 REGISTER LINK CHECK
TEST 172212 RESULT ----- 12:13:31 --> ***PASS

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TEST 172213 ALPG AGEN X-ALU SOURCE A FIELD CHECK
TEST 172213 RESULT ----- 12:13:31 --> ***PASS
TEST 172214 ALPG AGEN Y-ALU SOURCE A FIELD CHECK
TEST 172214 RESULT ----- 12:13:31 --> ***PASS
TEST 172215 ALPG AGEN X-ALU SOURCE B FIELD CHECK
TEST 172215 RESULT ----- 12:13:31 --> ***PASS
TEST 172216 ALPG AGEN Y-ALU SOURCE B FIELD CHECK
TEST 172216 RESULT ----- 12:13:31 --> ***PASS
TEST 172220 ALPG AGEN X-ALU OPERATION FIELD CHECK
TEST 172220 RESULT ----- 12:13:31 --> ***PASS
TEST 172221 ALPG AGEN Y-ALU OPERATION FIELD CHECK 1
TEST 172221 RESULT ----- 12:13:31 --> ***PASS
TEST 172222 ALPG AGEN Y-ALU OPERATION FIELD CHECK 2
TEST 172222 RESULT ----- 12:13:31 --> ***PASS
TEST 172223 ALPG AGEN Y-ALU OPERATION FIELD CHECK 3
TEST 172223 RESULT ----- 12:13:31 --> ***PASS
TEST 172224 ALPG AGEN Y-ALU OPERATION FIELD CHECK 4
TEST 172224 RESULT ----- 12:13:31 --> ***PASS
TEST 172225 ALPG AGEN Y-ALU OPERATION FIELD CHECK 5
TEST 172225 RESULT ----- 12:13:31 --> ***PASS
TEST 172226 ALPG AGEN X-ALU DESTINATION FIELD CHECK
TEST 172226 RESULT ----- 12:13:31 --> ***PASS
TEST 172227 ALPG AGEN Y-ALU DESTINATION FIELD CHECK
TEST 172227 RESULT ----- 12:13:31 --> ***PASS
TEST 172230 ALPG AGEN X-OUT (L<(XC,XB,XS)) DATA MUX CHECK
TEST 172230 RESULT ----- 12:13:31 --> ***PASS
TEST 172231 ALPG AGEN Y-OUT (H<(YC,YB,YS)) DATA MUX CHECK
TEST 172231 RESULT ----- 12:13:31 --> ***PASS
TEST 172232 ALPG AGEN XB CARRY CHECK
TEST 172232 RESULT ----- 12:13:31 --> ***PASS
TEST 172233 ALPG AGEN XC CARRY CHECK
TEST 172233 RESULT ----- 12:13:31 --> ***PASS
TEST 172234 ALPG AGEN YB CARRY CHECK
TEST 172234 RESULT ----- 12:13:31 --> ***PASS
TEST 172235 ALPG AGEN YC CARRY CHECK
TEST 172235 RESULT ----- 12:13:31 --> ***PASS
TEST 172236 ALPG AGEN LMAX<>HMAX INSTRUCTION CHECK
TEST 172236 RESULT ----- 12:13:31 --> ***PASS
TEST 172240 ALPG AGEN Z INSTRUCTION CHECK 1
TEST 172240 RESULT ----- 12:13:31 --> ***PASS
TEST 172241 ALPG AGEN Z INSTRUCTION CHECK 2
TEST 172241 RESULT ----- 12:13:31 --> ***PASS
TEST 172242 ALPG AGEN Z INSTRUCTION CHECK 3
TEST 172242 RESULT ----- 12:13:31 --> ***PASS
TEST 172243 ALPG AGEN N INSTRUCTION CHECK
TEST 172243 RESULT ----- 12:13:31 --> ***PASS
TEST 172244 ALPG AGEN X MUX (X<L,X<H,X<L'Z,X<Z) CHECK
TEST 172244 RESULT ----- 12:13:31 --> ***PASS
TEST 172245 ALPG AGEN Y MUX (Y<L,Y<H,Y<L'Z,Y<Z) CHECK
TEST 172245 RESULT ----- 12:13:31 --> ***PASS
TEST 172246 ALPG AGEN /X INSTRUCTION CHECK
TEST 172246 RESULT ----- 12:13:31 --> ***PASS
TEST 172247 ALPG AGEN /Y INSTRUCTION CHECK
TEST 172247 RESULT ----- 12:13:31 --> ***PASS
TEST 172250 ALPG AGEN XMAX MASK CHECK
TEST 172250 RESULT ----- 12:13:31 --> ***PASS
TEST 172251 ALPG AGEN YMAX MASK CHECK
TEST 172251 RESULT ----- 12:13:31 --> ***PASS
TEST 172253 ALPG AGEN X-Z MULTIPLEXER (XZ ENABLE) CHECK
TEST 172253 RESULT ----- 12:13:31 --> ***PASS
TEST 172254 ALPG AGEN Y-Z MULTIPLEXER (YZ ENABLE) CHECK
TEST 172254 RESULT ----- 12:13:31 --> ***PASS
TEST 172255 ALPG AGEN X-N MULTIPLEXER (XN ENABLE) CHECK
TEST 172255 RESULT ----- 12:13:31 --> ***PASS
TEST 172256 ALPG AGEN Y-N MULTIPLEXER (YN ENABLE) CHECK

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TEST 172256 RESULT ----- 12:13:31 --> ***PASS
TEST 172257 ALPG AGEN RF COUNTER CHECK
TEST 172257 RESULT ----- 12:13:31 --> ***PASS
TEST 172260 ALPG AGEN X OFFSET ADDRESS ADDER CHECK
TEST 172260 RESULT ----- 12:13:31 --> ***PASS
TEST 172261 ALPG AGEN Y OFFSET ADDRESS ADDER CHECK
TEST 172261 RESULT ----- 12:13:31 --> ***PASS
TEST 172286 ALPG AGEN PSCROFF/SCROFF OUTPUT CHECK
TEST 172286 RESULT ----- 12:13:31 --> ***PASS
TEST 172310 ALPG SCRAM FORMAT REG. R/W CHECK
TEST 172310 RESULT ----- 12:13:31 --> ***PASS
TEST 172311 ALPG SCRAM XSCRAM ADDRESS SELECT CHECK
TEST 172311 RESULT ----- 12:13:31 --> ***PASS
TEST 172312 ALPG SCRAM YSCRAM ADDRESS SELECT CHECK
TEST 172312 RESULT ----- 12:13:31 --> ***PASS
TEST 172313 ALPG SCRAM XSCRAM MULTIPLEX CHECK (UX0-15)
TEST 172313 RESULT ----- 12:13:32 --> ***PASS
TEST 172314 ALPG SCRAM YSCRAM MULTIPLEX CHECK (UY0-15)
TEST 172314 RESULT ----- 12:13:32 --> ***PASS
TEST 172315 ALPG SCRAM XSCRAM CONVERSION CHECK (UX0-15)
TEST 172315 RESULT ----- 12:13:32 --> ***PASS
TEST 172316 ALPG SCRAM YSCRAM CONVERSION CHECK (UY0-15)
TEST 172316 RESULT ----- 12:13:32 --> ***PASS
TEST 172320 ALPG SCRAM XSCRAM MULTIPLEX CHECK (FX0-15)
TEST 172320 RESULT ----- 12:13:32 --> ***PASS
TEST 172321 ALPG SCRAM YSCRAM MULTIPLEX CHECK (FY0-15)
TEST 172321 RESULT ----- 12:13:32 --> ***PASS
TEST 172323 ALPG SCRAM REAL TIME OFF CHECK 1 (UX,UY)
TEST 172323 RESULT ----- 12:13:32 --> ***PASS
TEST 172324 ALPG SCRAM REAL TIME OFF CHECK 2 (FX,FY)
TEST 172324 RESULT ----- 12:13:32 --> ***PASS
TEST 172325 ALPG SCRAM XSCRAM CONVERSION CHECK (FX0-15)
TEST 172325 RESULT ----- 12:13:32 --> ***PASS
TEST 172326 ALPG SCRAM YSCRAM CONVERSION CHECK (FY0-15)
TEST 172326 RESULT ----- 12:13:32 --> ***PASS
TEST 172330 ALPG SCRAM XSYNC MODE FUNCTION CHECK
TEST 172330 RESULT ----- 12:13:32 --> ***PASS
TEST 172331 ALPG SCRAM YSYNC MODE FUNCTION CHECK
TEST 172331 RESULT ----- 12:13:32 --> ***PASS
TEST 172332 ALPG SCRAM SYNC MODE FUNCTION CHECK
TEST 172332 RESULT ----- 12:13:32 --> ***PASS
TEST 172340 ALPG SCRAM XWINDOW FUNCTION CHECK
TEST 172340 RESULT ----- 12:13:32 --> ***PASS
TEST 172341 ALPG SCRAM YWINDOW FUNCTION CHECK
TEST 172341 RESULT ----- 12:13:32 --> ***PASS
TEST 172342 ALPG SCRAM WINDOW MODE FUNCTION CHECK
TEST 172342 RESULT ----- 12:13:32 --> ***PASS
TEST 172400 ALPG DGEN SET INSTRUCTION CHECK 1
TEST 172400 RESULT ----- 12:13:32 --> ***PASS
TEST 172401 ALPG DGEN SET INSTRUCTION CHECK 2
TEST 172401 RESULT ----- 12:13:32 --> ***PASS
TEST 172402 ALPG DGEN TP1 REGISTER FUNCTION CHECK
TEST 172402 RESULT ----- 12:13:32 --> ***PASS
TEST 172403 ALPG DGEN TP2 REGISTER FUNCTION CHECK
TEST 172403 RESULT ----- 12:13:32 --> ***PASS
TEST 172404 ALPG DGEN /D1 & DCMR1 FUNCTION CHECK
TEST 172404 RESULT ----- 12:13:32 --> ***PASS
TEST 172405 ALPG DGEN /D2 & DCMR2 FUNCTION CHECK
TEST 172405 RESULT ----- 12:13:32 --> ***PASS
TEST 172406 ALPG DGEN DATA HOLD INSTRUCTION (PD) CHECK
TEST 172406 RESULT ----- 12:13:32 --> ***PASS
TEST 172407 ALPG DGEN DATA PARITY MODE CHECK (MODE PARITY)
TEST 172407 RESULT ----- 12:13:32 --> ***PASS
TEST 172410 ALPG DGEN DSD,RCD,CCD REG. FUNCTION CHECK
TEST 172410 RESULT ----- 12:13:32 --> ***PASS

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TEST 172411 ALPG DGEN FP0 FUNCTION CHECK
TEST 172411 RESULT ----- 12:13:32 --> ***PASS
TEST 172412 ALPG DGEN FP1 (X0.XOR.Y0) FUNCTION CHECK
TEST 172412 RESULT ----- 12:13:32 --> ***PASS
TEST 172413 ALPG DGEN FP3 (X=Y+DSD) FUNCTION CHECK
TEST 172413 RESULT ----- 12:13:32 --> ***PASS
TEST 172414 ALPG DGEN FP4 (X=/(Y+DSD)) FUNCTION CHECK
TEST 172414 RESULT ----- 12:13:32 --> ***PASS
TEST 172415 ALPG DGEN FP7 (MASK PARITY) FUNCTION CHECK
TEST 172415 RESULT ----- 12:13:32 --> ***PASS
TEST 172416 ALPG DGEN FP8 (X=RCD) FUCNTION CHECK
TEST 172416 RESULT ----- 12:13:32 --> ***PASS
TEST 172417 ALPG DGEN FP9 (Y=CCD) FUNCTION CHECK
TEST 172417 RESULT ----- 12:13:32 --> ***PASS
TEST 172418 ALPG DGEN FP10 (X=RCD.AND.Y=CCD) FUNCTION CHECK
TEST 172418 RESULT ----- 12:13:32 --> ***PASS
TEST 172419 ALPG DGEN FP FUNCTION DATA INVERT CHECK
TEST 172419 RESULT ----- 12:13:32 --> ***PASS
TEST 172420 ALPG DGEN ARIRAM ADDRESS FORMAT REG R/W CHECK
TEST 172420 RESULT ----- 12:13:32 --> ***PASS
TEST 172421 ALPG DGEN ARIRAM ADDRESS SELECT FUNCTION CHECK
TEST 172421 RESULT ----- 12:13:32 --> ***PASS
TEST 172422 ALPG DGEN ARIRAM DATA INVERT CHECK
TEST 172422 RESULT ----- 12:13:33 --> ***PASS
TEST 172423 ALPG DGEN ARIRAM DATA BIT CHECK
TEST 172423 RESULT ----- 12:13:33 --> ***PASS
TEST 172424 ALPG DGEN ARIRAM SIGNAL HOLD CHECK
TEST 172424 RESULT ----- 12:13:33 --> ***PASS
TEST 172425 ALPG DGEN ARIRAM INVERT REAL TIME INHIBIT CHECK
TEST 172425 RESULT ----- 12:13:33 --> ***PASS
TEST 172430 ALPG DGEN TPXOR FUNCTION CHECK
TEST 172430 RESULT ----- 12:13:33 --> ***PASS
TEST 172433 ALPG DGEN DSYNC FUNCTION CHECK
TEST 172433 RESULT ----- 12:13:33 --> ***PASS
TEST 172435 ALPG DGEN DSEL (TP1/2/XOR) FUNCTION CHECK
TEST 172435 RESULT ----- 12:13:33 --> ***PASS
TEST 172436 ALPG DGEN TPS1 FUNCTION CHECK
TEST 172436 RESULT ----- 12:13:33 --> ***PASS
TEST 172437 ALPG DGEN TPS2 FUNCTION CHECK
TEST 172437 RESULT ----- 12:13:33 --> ***PASS
TEST 172501 ALPG CDB DA/DB DATA OUTPUT CHECK
TEST 172501 RESULT ----- 12:13:33 --> ***PASS
TEST 172513 ALPG CDB MUT SYNC SIGNAL SELECT CHECK
TEST 172513 RESULT ----- 12:13:33 --> ***PASS
TEST 172514 ALPG CDB MUT CONTROL BIT OUTPUT CHECK
TEST 172514 RESULT ----- 12:13:33 --> ***PASS
TEST 172520 ALPG CDB PREF FUNCTION CHECK
TEST 172520 RESULT ----- 12:13:33 --> ***PASS
TEST 172521 ALPG CDB CPE1-4,DRE1-2,RINV BIT CHECK
TEST 172521 RESULT ----- 12:13:33 --> ***PASS
TEST 172522 ALPG CDB CPE1-4,DRE1-2,RINV SELECT CHECK
TEST 172522 RESULT ----- 12:13:33 --> ***PASS
TEST 172523 ALPG CDB WINDOW MODE CHECK
TEST 172523 RESULT ----- 12:13:33 --> ***PASS
TEST 172525 ALPG CDB SYNC SELECT CHECK
TEST 172525 RESULT ----- 12:13:33 --> ***PASS
TEST 173000 ALPG PATTERN OUTPUT CHECK
TEST 173000 RESULT ----- 12:13:33 --> ***PASS
TEST 173300 ALPG CONCURRENT PC LINE CHECK
TEST 173300 RESULT ----- 12:13:33 --> ***PASS
TEST 173310 ALPG CONCURRENT PC BASE SUB. CHECK
TEST 173310 RESULT ----- 12:13:33 --> ***PASS
TEST 173320 ALPG CONCURRENT FATEST CHECK
TEST 173320 RESULT ----- 12:13:33 --> ***PASS
TEST 173330 ALPG CONCURRENT REFRESH A CHECK

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TEST 173330 RESULT ----- 12:13:33 --> ***PASS
TEST 173340 ALPG CONCURRENT REFRESH B CHECK
TEST 173340 RESULT ----- 12:13:33 --> ***PASS
TEST 173350 ALPG CONCURRENT REFRESH FIFO CHECK
TEST 173350 RESULT ----- 12:13:33 --> ***PASS
TEST 173360 ALPG CONCURRENT BASE & STEA CHECK
TEST 173360 RESULT ----- 12:13:33 --> ***PASS
TEST 173370 ALPG CONCURRENT INT & ATEST=0 CHECK
TEST 173370 RESULT ----- 12:13:33 --> ***PASS
TEST 173400 ALPG CONCURRENT REFRESH SPA/SPC COMBI CHECK
TEST 173400 RESULT ----- 12:13:33 --> ***PASS
TEST 173500 ALPG DFM STORE CHECK (CONCURRENT ALL UP TO PCNT)
TEST 173500 RESULT ----- 12:13:33 --> ***PASS
TEST 173520 ALPG DFM STORE CHECK (CONCURRENT STATE UP TO PCNT)
TEST 173520 RESULT ----- 12:13:33 --> ***PASS
TEST 173540 ALPG DFM STORE CHECK (CONCURRENT FAIL UP TO PCNT)
TEST 173540 RESULT ----- 12:13:33 --> ***PASS
TEST 173550 ALPG DFM STORE CHECK (CONCURRENT STATE UP TO FCNT)
TEST 173550 RESULT ----- 12:13:33 --> ***PASS
TEST 173600 ALPG TTB CONTROL SIG STORE CHECK
TEST 173600 RESULT ----- 12:13:33 --> ***PASS
TEST 175001 ALPG2 REGISTER R/W CHECK
TEST 175001 RESULT ----- 12:13:33 --> ***PASS
TEST 175002 ALPG2 WCS R/W CHECK
TEST 175002 RESULT ----- 12:13:33 --> ***PASS
TEST 175006 ALPG2 UNIT SEL FUNCTION CHECK
TEST 175006 RESULT ----- 12:13:33 --> ***PASS
TEST 175007 ALPG2 ALPG2 MEMORY SELECT CHECK
TEST 175007 RESULT ----- 12:13:33 --> ***PASS
TEST 175011 ALPG2 WCS1-10 R/W CHECK (MARCHING)
TEST 175011 RESULT ----- 12:13:33 --> ***PASS
TEST 175014 ALPG2 XSCRAM R/W CHECK (MARCHING)
TEST 175014 RESULT ----- 12:13:34 --> ***PASS
TEST 175015 ALPG2 YSCRAM R/W CHECK (MARCHING)
TEST 175015 RESULT ----- 12:13:35 --> ***PASS
TEST 175016 ALPG2 ARIRAM R/W CHECK (MARCHING)
TEST 175016 RESULT ----- 12:13:35 --> ***PASS
TEST 175020 ALPG2 TBRST (TESTER BUS RESET) CHECK
TEST 175020 RESULT ----- 12:13:35 --> ***PASS
TEST 175021 ALPG2 PG RESET (CONTROL REG.) CHECK
TEST 175021 RESULT ----- 12:13:35 --> ***PASS
TEST 175031 ALPG2 WCS11 R/W CHECK
TEST 175031 RESULT ----- 12:13:35 --> ***PASS
TEST 175200 ALPG2 AGEN SET INSTRUCTION CHECK 1
TEST 175200 RESULT ----- 12:13:35 --> ***PASS
TEST 175201 ALPG2 AGEN SET INSTRUCTION CHECK 2
TEST 175201 RESULT ----- 12:13:35 --> ***PASS
TEST 175202 ALPG2 AGEN XT FUNCTION CHECK
TEST 175202 RESULT ----- 12:13:35 --> ***PASS
TEST 175203 ALPG2 AGEN YT FUNCTION CHECK
TEST 175203 RESULT ----- 12:13:35 --> ***PASS
TEST 175204 ALPG2 AGEN XB INSTRUCTION CHECK
TEST 175204 RESULT ----- 12:13:35 --> ***PASS
TEST 175205 ALPG2 AGEN YB INSTRUCTION CHECK
TEST 175205 RESULT ----- 12:13:36 --> ***PASS
TEST 175210 ALPG2 AGEN D3 INSTRUCTION CHECK
TEST 175210 RESULT ----- 12:13:36 --> ***PASS
TEST 175211 ALPG2 AGEN D4 INSTRUCTION CHECK
TEST 175211 RESULT ----- 12:13:36 --> ***PASS
TEST 175212 ALPG2 AGEN D3 D4 REGISTER LINK CHECK
TEST 175212 RESULT ----- 12:13:36 --> ***PASS
TEST 175213 ALPG2 AGEN X-ALU SOURCE A FIELD CHECK
TEST 175213 RESULT ----- 12:13:36 --> ***PASS
TEST 175214 ALPG2 AGEN Y-ALU SOURCE A FIELD CHECK
TEST 175214 RESULT ----- 12:13:36 --> ***PASS

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TEST 175215 ALPG2 AGEN X-ALU SOURCE B FIELD CHECK
TEST 175215 RESULT ----- 12:13:36 --> ***PASS
TEST 175216 ALPG2 AGEN Y-ALU SOURCE B FIELD CHECK
TEST 175216 RESULT ----- 12:13:36 --> ***PASS
TEST 175220 ALPG2 AGEN X-ALU OPERATION FIELD CHECK
TEST 175220 RESULT ----- 12:13:36 --> ***PASS
TEST 175221 ALPG2 AGEN Y-ALU OPERATION FIELD CHECK 1
TEST 175221 RESULT ----- 12:13:36 --> ***PASS
TEST 175222 ALPG2 AGEN Y-ALU OPERATION FIELD CHECK 2
TEST 175222 RESULT ----- 12:13:36 --> ***PASS
TEST 175223 ALPG2 AGEN Y-ALU OPERATION FIELD CHECK 3
TEST 175223 RESULT ----- 12:13:36 --> ***PASS
TEST 175224 ALPG2 AGEN Y-ALU OPERATION FIELD CHECK 4
TEST 175224 RESULT ----- 12:13:36 --> ***PASS
TEST 175225 ALPG2 AGEN Y-ALU OPERATION FIELD CHECK 5
TEST 175225 RESULT ----- 12:13:36 --> ***PASS
TEST 175226 ALPG2 AGEN X-ALU DESTINATION FIELD CHECK
TEST 175226 RESULT ----- 12:13:36 --> ***PASS
TEST 175227 ALPG2 AGEN Y-ALU DESTINATION FIELD CHECK
TEST 175227 RESULT ----- 12:13:36 --> ***PASS
TEST 175230 ALPG2 AGEN X-OUT (L<(XC,XB,XS)) DATA MUX CHECK
TEST 175230 RESULT ----- 12:13:36 --> ***PASS
TEST 175231 ALPG2 AGEN Y-OUT (H<(YC,YB,YS)) DATA MUX CHECK
TEST 175231 RESULT ----- 12:13:36 --> ***PASS
TEST 175232 ALPG2 AGEN XB CARRY CHECK
TEST 175232 RESULT ----- 12:13:36 --> ***PASS
TEST 175233 ALPG2 AGEN XC CARRY CHECK
TEST 175233 RESULT ----- 12:13:36 --> ***PASS
TEST 175234 ALPG2 AGEN YB CARRY CHECK
TEST 175234 RESULT ----- 12:13:36 --> ***PASS
TEST 175235 ALPG2 AGEN YC CARRY CHECK
TEST 175235 RESULT ----- 12:13:36 --> ***PASS
TEST 175236 ALPG2 AGEN LMAX<>HMAX INSTRUCTION CHECK
TEST 175236 RESULT ----- 12:13:36 --> ***PASS
TEST 175240 ALPG2 AGEN Z INSTRUCTION CHECK 1
TEST 175240 RESULT ----- 12:13:36 --> ***PASS
TEST 175241 ALPG2 AGEN Z INSTRUCTION CHECK 2
TEST 175241 RESULT ----- 12:13:36 --> ***PASS
TEST 175242 ALPG2 AGEN Z INSTRUCTION CHECK 3
TEST 175242 RESULT ----- 12:13:36 --> ***PASS
TEST 175243 ALPG2 AGEN N INSTRUCTION CHECK
TEST 175243 RESULT ----- 12:13:36 --> ***PASS
TEST 175244 ALPG2 AGEN X MUX (X<L,X<H,X<L'Z,X<Z) CHECK
TEST 175244 RESULT ----- 12:13:36 --> ***PASS
TEST 175245 ALPG2 AGEN Y MUX (Y<L,Y<H,Y<L'Z,Y<Z) CHECK
TEST 175245 RESULT ----- 12:13:36 --> ***PASS
TEST 175246 ALPG2 AGEN /X INSTRUCTION CHECK
TEST 175246 RESULT ----- 12:13:36 --> ***PASS
TEST 175247 ALPG2 AGEN /Y INSTRUCTION CHECK
TEST 175247 RESULT ----- 12:13:36 --> ***PASS
TEST 175250 ALPG2 AGEN XMAX MASK CHECK
TEST 175250 RESULT ----- 12:13:36 --> ***PASS
TEST 175251 ALPG2 AGEN YMAX MASK CHECK
TEST 175251 RESULT ----- 12:13:36 --> ***PASS
TEST 175253 ALPG2 AGEN X-Z MULTIPLEXER (XZ ENABLE) CHECK
TEST 175253 RESULT ----- 12:13:36 --> ***PASS
TEST 175254 ALPG2 AGEN Y-Z MULTIPLEXER (YZ ENABLE) CHECK
TEST 175254 RESULT ----- 12:13:36 --> ***PASS
TEST 175255 ALPG2 AGEN X-N MULTIPLEXER (XN ENABLE) CHECK
TEST 175255 RESULT ----- 12:13:36 --> ***PASS
TEST 175256 ALPG2 AGEN Y-N MULTIPLEXER (YN ENABLE) CHECK
TEST 175256 RESULT ----- 12:13:36 --> ***PASS
TEST 175257 ALPG2 AGEN RF COUNTER CHECK
TEST 175257 RESULT ----- 12:13:36 --> ***PASS
TEST 175260 ALPG2 AGEN X OFFSET ADDRESS ADDER CHECK

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TEST 175260 RESULT ----- 12:13:36 --> ***PASS
TEST 175261 ALPG2 AGEN Y OFFSET ADDRESS ADDER CHECK
TEST 175261 RESULT ----- 12:13:36 --> ***PASS
TEST 175286 ALPG2 AGEN PSCROFF/SCROFF OUTPUT CHECK
TEST 175286 RESULT ----- 12:13:36 --> ***PASS
TEST 175310 ALPG2 SCRAM FORMAT REG. R/W CHECK
TEST 175310 RESULT ----- 12:13:36 --> ***PASS
TEST 175311 ALPG2 SCRAM XSCRAM ADDRESS SELECT CHECK
TEST 175311 RESULT ----- 12:13:36 --> ***PASS
TEST 175312 ALPG2 SCRAM YSCRAM ADDRESS SELECT CHECK
TEST 175312 RESULT ----- 12:13:36 --> ***PASS
TEST 175313 ALPG2 SCRAM XSCRAM MULTIPLEX CHECK (UX0-15)
TEST 175313 RESULT ----- 12:13:36 --> ***PASS
TEST 175314 ALPG2 SCRAM YSCRAM MULTIPLEX CHECK (UY0-15)
TEST 175314 RESULT ----- 12:13:36 --> ***PASS
TEST 175315 ALPG2 SCRAM XSCRAM CONVERSION CHECK (UX0-15)
TEST 175315 RESULT ----- 12:13:37 --> ***PASS
TEST 175316 ALPG2 SCRAM YSCRAM CONVERSION CHECK (UY0-15)
TEST 175316 RESULT ----- 12:13:37 --> ***PASS
TEST 175320 ALPG2 SCRAM XSCRAM MULTIPLEX CHECK (FX0-15)
TEST 175320 RESULT ----- 12:13:37 --> ***PASS
TEST 175321 ALPG2 SCRAM YSCRAM MULTIPLEX CHECK (FY0-15)
TEST 175321 RESULT ----- 12:13:37 --> ***PASS
TEST 175323 ALPG2 SCRAM REAL TIME OFF CHECK 1 (UX,UY)
TEST 175323 RESULT ----- 12:13:37 --> ***PASS
TEST 175324 ALPG2 SCRAM REAL TIME OFF CHECK 2 (FX,FY)
TEST 175324 RESULT ----- 12:13:37 --> ***PASS
TEST 175325 ALPG2 SCRAM XSCRAM CONVERSION CHECK (FX0-15)
TEST 175325 RESULT ----- 12:13:37 --> ***PASS
TEST 175326 ALPG2 SCRAM YSCRAM CONVERSION CHECK (FY0-15)
TEST 175326 RESULT ----- 12:13:37 --> ***PASS
TEST 175330 ALPG2 SCRAM XSYNC MODE FUNCTION CHECK
TEST 175330 RESULT ----- 12:13:37 --> ***PASS
TEST 175331 ALPG2 SCRAM YSYNC MODE FUNCTION CHECK
TEST 175331 RESULT ----- 12:13:37 --> ***PASS
TEST 175332 ALPG2 MAIN SCRAM SYNC MODE FUNCTION CHECK
TEST 175332 RESULT ----- 12:13:37 --> ***PASS
TEST 175340 ALPG2 SCRAM XWINDOW FUNCTION CHECK
TEST 175340 RESULT ----- 12:13:37 --> ***PASS
TEST 175341 ALPG2 SCRAM YWINDOW FUNCTION CHECK
TEST 175341 RESULT ----- 12:13:37 --> ***PASS
TEST 175342 ALPG2 SCRAM WINDOW MODE FUNCTION CHECK
TEST 175342 RESULT ----- 12:13:37 --> ***PASS
TEST 175400 ALPG2 DGEN SET INSTRUCTION CHECK 1
TEST 175400 RESULT ----- 12:13:37 --> ***PASS
TEST 175401 ALPG2 DGEN SET INSTRUCTION CHECK 2
TEST 175401 RESULT ----- 12:13:37 --> ***PASS
TEST 175402 ALPG2 DGEN TP1 REGISTER FUNCTION CHECK
TEST 175402 RESULT ----- 12:13:37 --> ***PASS
TEST 175403 ALPG2 DGEN TP2 REGISTER FUNCTION CHECK
TEST 175403 RESULT ----- 12:13:37 --> ***PASS
TEST 175404 ALPG2 DGEN /D1 & DCMR1 FUNCTION CHECK
TEST 175404 RESULT ----- 12:13:37 --> ***PASS
TEST 175405 ALPG2 DGEN /D2 & DCMR2 FUNCTION CHECK
TEST 175405 RESULT ----- 12:13:37 --> ***PASS
TEST 175406 ALPG2 DGEN DATA HOLD INSTRUCTION (PD) CHECK
TEST 175406 RESULT ----- 12:13:37 --> ***PASS
TEST 175407 ALPG2 DGEN DATA PARITY MODE CHECK (MODE PARITY)
TEST 175407 RESULT ----- 12:13:37 --> ***PASS
TEST 175410 ALPG2 DGEN DSD,RCD,CCD REG. FUNCTION CHECK
TEST 175410 RESULT ----- 12:13:37 --> ***PASS
TEST 175411 ALPG2 DGEN FP0 FUNCTION CHECK
TEST 175411 RESULT ----- 12:13:37 --> ***PASS
TEST 175412 ALPG2 DGEN FP1 (X0.XOR.Y0) FUNCTION CHECK
TEST 175412 RESULT ----- 12:13:37 --> ***PASS

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TEST 175413 ALPG2 DGEN FP3 (X=Y+DSD) FUNCTION CHECK
TEST 175413 RESULT ----- 12:13:37 --> ***PASS
TEST 175414 ALPG2 DGEN FP4 (X=/(Y+DSD)) FUNCTION CHECK
TEST 175414 RESULT ----- 12:13:37 --> ***PASS
TEST 175415 ALPG2 DGEN FP7 (MASK PARITY) FUNCTION CHECK
TEST 175415 RESULT ----- 12:13:37 --> ***PASS
TEST 175416 ALPG2 DGEN FP8 (X=RCD) FUCNTION CHECK
TEST 175416 RESULT ----- 12:13:37 --> ***PASS
TEST 175417 ALPG2 DGEN FP9 (Y=CCD) FUNCTION CHECK
TEST 175417 RESULT ----- 12:13:37 --> ***PASS
TEST 175418 ALPG2 DGEN FP10 (X=RCD.AND.Y=CCD) FUNCTION CHECK
TEST 175418 RESULT ----- 12:13:37 --> ***PASS
TEST 175419 ALPG2 DGEN FP FUNCTION DATA INVERT CHECK
TEST 175419 RESULT ----- 12:13:37 --> ***PASS
TEST 175420 ALPG2 DGEN ARIRAM ADDRESS FORMAT REG R/W CHECK
TEST 175420 RESULT ----- 12:13:37 --> ***PASS
TEST 175421 ALPG2 DGEN ARIRAM ADDRESS SELECT FUNCTION CHECK
TEST 175421 RESULT ----- 12:13:37 --> ***PASS
TEST 175422 ALPG2 DGEN ARIRAM DATA INVERT CHECK
TEST 175422 RESULT ----- 12:13:37 --> ***PASS
TEST 175423 ALPG2 DGEN ARIRAM DATA BIT CHECK
TEST 175423 RESULT ----- 12:13:37 --> ***PASS
TEST 175424 ALPG2 DGEN ARIRAM SIGNAL HOLD CHECK
TEST 175424 RESULT ----- 12:13:37 --> ***PASS
TEST 175425 ALPG2 DGEN ARIRAM INVERT REAL TIME INHIBIT CHECK
TEST 175425 RESULT ----- 12:13:37 --> ***PASS
TEST 175430 ALPG2 DGEN TPXOR FUNCTION CHECK
TEST 175430 RESULT ----- 12:13:37 --> ***PASS
TEST 175433 ALPG2 DGEN DSYNC FUNCTION CHECK
TEST 175433 RESULT ----- 12:13:37 --> ***PASS
TEST 175435 ALPG2 DGEN DSEL (TP1/2/XOR) FUNCTION CHECK
TEST 175435 RESULT ----- 12:13:37 --> ***PASS
TEST 175436 ALPG2 DGEN TPS1 FUNCTION CHECK
TEST 175436 RESULT ----- 12:13:37 --> ***PASS
TEST 175437 ALPG2 DGEN TPS2 FUNCTION CHECK
TEST 175437 RESULT ----- 12:13:37 --> ***PASS
TEST 176000 ALPG2 PATTERN OUTPUT CHECK
TEST 176000 RESULT ----- 12:13:37 --> ***PASS
TEST 176300 ALPG2 CONCURRENT PC LINE CHECK
TEST 176300 RESULT ----- 12:13:37 --> ***PASS
TEST 176310 ALPG2 CONCURRENT PC BASE SUB. CHECK
TEST 176310 RESULT ----- 12:13:37 --> ***PASS
TEST 176320 ALPG2 CONCURRENT FATEST CHECK
TEST 176320 RESULT ----- 12:13:37 --> ***PASS
TEST 176330 ALPG2 CONCURRENT REFRESH A CHECK
TEST 176330 RESULT ----- 12:13:37 --> ***PASS
TEST 176340 ALPG2 CONCURRENT REFRESH B CHECK
TEST 176340 RESULT ----- 12:13:37 --> ***PASS
TEST 176350 ALPG2 CONCURRENT REFRESH FIFO CHECK
TEST 176350 RESULT ----- 12:13:37 --> ***PASS
TEST 176360 ALPG2 CONCURRENT BASE & STEA CHECK
TEST 176360 RESULT ----- 12:13:37 --> ***PASS
TEST 176370 ALPG2 CONCURRENT INT & ATEST=0 CHECK
TEST 176370 RESULT ----- 12:13:37 --> ***PASS
TEST 174010 AFM MEMORY SENSE CHECK
TEST 174010 RESULT ----- 12:13:39 --> ***PASS
TEST 174011 AFM REGISTER READ/WRITE & RESET CHECK 1
TEST 174011 RESULT ----- 12:13:39 --> ***PASS
TEST 174012 AFM REGISTER READ/WRITE & RESET CHECK 2
TEST 174012 RESULT ----- 12:13:39 --> ***PASS
TEST 174013 AFM REGISTER READ/WRITE & RESET CHECK 3
TEST 174013 RESULT ----- 12:13:39 --> ***PASS
TEST 174014 AFM REGISTER READ/WRITE & RESET CHECK 4
TEST 174014 RESULT ----- 12:13:39 --> ***PASS
TEST 174015 AFM REGISTER READ/WRITE & RESET CHECK 5

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TEST 174015 RESULT ----- 12:13:39 --> ***PASS
TEST 174016 AFM ADDRESS POINTER READ/WRITE & RESET CHECK
TEST 174016 RESULT ----- 12:13:39 --> ***PASS
TEST 174017 AFM FM ADDRESS SEL READ/WRITE CHECK 1
TEST 174017 RESULT ----- 12:13:39 --> ***PASS
TEST 174018 AFM FM ADDRESS SEL READ/WRITE CHECK 2
TEST 174018 RESULT ----- 12:13:39 --> ***PASS
TEST 174019 AFM FM ADDRESS SEL READ/WRITE CHECK 3
TEST 174019 RESULT ----- 12:13:39 --> ***PASS
TEST 174020 AFM FMAP READ/WRITE & RESET CHECK
TEST 174020 RESULT ----- 12:13:39 --> ***PASS
TEST 174021 AFM FMAP COUNT CHECK 1
TEST 174021 RESULT ----- 12:13:39 --> ***PASS
TEST 174022 AFM FMAP COUNT CHECK 2
TEST 174022 RESULT ----- 12:13:39 --> ***PASS
TEST 174023 AFM ADDRESS SELECT FOR R/W CHECK 1
TEST 174023 RESULT ----- 12:13:39 --> ***PASS
TEST 174024 AFM ADDRESS SELECT FOR R/W CHECK 2
TEST 174024 RESULT ----- 12:13:39 --> ***PASS
TEST 174025 AFM PFC READ/WRITE & RESET CHECK
TEST 174025 RESULT ----- 12:13:39 --> ***PASS
TEST 174026 AFM MDIAG MEMORY READ/WRITE CHECK
TEST 174026 RESULT ----- 12:13:39 --> ***PASS
TEST 174027 AFM FMAP_STACK FOR AP READ/WRITE & RESET CHECK
TEST 174027 RESULT ----- 12:13:39 --> ***PASS
TEST 174028 AFM FMAP_STACK REGISTER READ/WRITE & RESET CHECK
TEST 174028 RESULT ----- 12:13:39 --> ***PASS
TEST 174029 AFM DFMAP COUNT CHECK
TEST 174029 RESULT ----- 12:13:39 --> ***PASS
TEST 174040 AFM READ/WRITE CHECK 1 (DATA CHECK)
TEST 174040 RESULT ----- 12:13:39 --> ***PASS
TEST 174041 AFM READ/WRITE CHECK 2 (ADDRESS CHECK)
TEST 174041 RESULT ----- 12:13:39 --> ***PASS
TEST 174042 AFM READ/WRITE CHECK 3 (BLOCK MARCHING)
TEST 174042 RESULT ----- 12:13:39 --> ***PASS
TEST 174043 AFM READ/WRITE CHECK 4 (HS-W/LS-R)
TEST 174043 RESULT ----- 12:13:39 --> ***PASS
TEST 174044 AFM READ/WRITE CHECK 5 (LS-W/HS-R)
TEST 174044 RESULT ----- 12:13:39 --> ***PASS
TEST 174045 AFM READ/WRITE CHECK 6 (X18-W/X1,2,4,9-R)
TEST 174045 RESULT ----- 12:13:39 --> ***PASS
TEST 174046 AFM READ/WRITE CHECK 7 (X1,2,4,9-W/X18-R)
TEST 174046 RESULT ----- 12:13:39 --> ***PASS
TEST 174047 AFM READ/WRITE CHECK 8 (BLOCK LINK)
TEST 174047 RESULT ----- 12:13:39 --> ***PASS
TEST 174048 AFM WRITE ENABLE BIT CHECK
TEST 174048 RESULT ----- 12:13:39 --> ***PASS
TEST 174049 AFM WRITE BLOCK SELECT CHECK
TEST 174049 RESULT ----- 12:13:39 --> ***PASS
TEST 174050 AFM READ BLOCK SELECT CHECK
TEST 174050 RESULT ----- 12:13:39 --> ***PASS
TEST 174051 AFM READ FORMATTER CHECK 1 (ORDATA)
TEST 174051 RESULT ----- 12:13:39 --> ***PASS
TEST 174052 AFM READ FORMATTER CHECK 2 (BIT-N)
TEST 174052 RESULT ----- 12:13:39 --> ***PASS
TEST 174053 AFM READ FORMATTER CHECK 3 (APOINT-OR 1)
TEST 174053 RESULT ----- 12:13:39 --> ***PASS
TEST 174054 AFM READ FORMATTER CHECK 4 (APOINT-OR 2)
TEST 174054 RESULT ----- 12:13:39 --> ***PASS
TEST 174060 AFM DFM READ/WRITE CHECK
TEST 174060 RESULT ----- 12:13:39 --> ***PASS
TEST 174070 AFM CLEAR MEMORY FUNCTION CHECK (ALL)
TEST 174070 RESULT ----- 12:13:39 --> ***PASS
TEST 174071 AFM CLEAR MEMORY FUNCTION CHECK (MODE)
TEST 174071 RESULT ----- 12:13:39 --> ***PASS

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TEST 174072 AFM PRESET MEMORY FUNCTION CHECK
TEST 174072 RESULT ----- 12:13:39 --> ***PASS
TEST 174073 AFM RESET MEMORY FUNCTION CHECK
TEST 174073 RESULT ----- 12:13:39 --> ***PASS
TEST 174074 AFM FAIL ADDRESS SEARCH CHECK
TEST 174074 RESULT ----- 12:13:39 --> ***PASS
TEST 174075 AFM FAIL ADDRESS SEARCH CHECK 2
TEST 174075 RESULT ----- 12:13:39 --> ***PASS
TEST 174076 AFM FAIL ADDRESS SEARCH CHECK 3
TEST 174076 RESULT ----- 12:13:39 --> ***PASS
TEST 174077 AFM HS FAIL ADDRESS SEARCH CHECK 1
TEST 174077 RESULT ----- 12:13:39 --> ***PASS
TEST 174078 AFM HS FAIL ADDRESS SEARCH CHECK 2
TEST 174078 RESULT ----- 12:13:39 --> ***PASS
TEST 174079 AFM HS FAIL ADDRESS SEARCH CHECK 3
TEST 174079 RESULT ----- 12:13:39 --> ***PASS
TEST 174080 AFM PARALLEL FAIL COUNTER CHECK 1
TEST 174080 RESULT ----- 12:13:39 --> ***PASS
TEST 174081 AFM PARALLEL FAIL COUNTER CHECK 2
TEST 174081 RESULT ----- 12:13:39 --> ***PASS
TEST 174082 AFM FAIL COUNTER CHECK 1
TEST 174082 RESULT ----- 12:13:39 --> ***PASS
TEST 174083 AFM FAIL COUNTER CHECK 2
TEST 174083 RESULT ----- 12:13:39 --> ***PASS
TEST 174084 AFM MEMORY DIAG FUNCTION PRECHECK 1
TEST 174084 RESULT ----- 12:13:39 --> ***PASS
TEST 174085 AFM MEMORY DIAG FUNCTION PRECHECK 2
TEST 174085 RESULT ----- 12:13:39 --> ***PASS
TEST 174086 AFM MEMORY DIAG FUNCTION PRECHECK 3
TEST 174086 RESULT ----- 12:13:39 --> ***PASS
TEST 174087 AFM MEMORY DIAGNOSIS
TEST 174087 RESULT ----- 12:13:40 --> ***PASS
TEST 174100 AFM ADDRESS INPUT CHECK
TEST 174100 RESULT ----- 12:13:40 --> ***PASS
TEST 174101 AFM ADDRESS DELAY CHECK
TEST 174101 RESULT ----- 12:13:40 --> ***PASS
TEST 174102 AFM ADDRESS INTERLEAVE CHECK
TEST 174102 RESULT ----- 12:13:40 --> ***PASS
TEST 174103 AFM ADDRESS SELECT CHECK 1
TEST 174103 RESULT ----- 12:13:40 --> ***PASS
TEST 174104 AFM ADDRESS SELECT CHECK 2
TEST 174104 RESULT ----- 12:13:40 --> ***PASS
TEST 174105 AFM FATST INPUT CHECK
TEST 174105 RESULT ----- 12:13:40 --> ***PASS
TEST 174106 AFM FATST INTERLEAVE CHECK
TEST 174106 RESULT ----- 12:13:40 --> ***PASS
TEST 174110 AFM EVSTORE INPUT CHECK
TEST 174110 RESULT ----- 12:13:40 --> ***PASS
TEST 174111 AFM EVSTORE INTERLEAVE CHECK
TEST 174111 RESULT ----- 12:13:40 --> ***PASS
TEST 174120 AFM FAIL INPUT CHECK
TEST 174120 RESULT ----- 12:13:41 --> ***PASS
TEST 174121 AFM FAIL OUTPUT CHECK
TEST 174121 RESULT ----- 12:13:41 --> ***PASS
TEST 174122 AFM FAIL INTERLEAVE CHECK
TEST 174122 RESULT ----- 12:13:41 --> ***PASS
TEST 174140 AFM FAIL STORE CHECK 1 (HS)
TEST 174140 RESULT ----- 12:13:42 --> ***PASS
TEST 174141 AFM FAIL STORE CHECK 2 (ADDRESS)
TEST 174141 RESULT ----- 12:13:42 --> ***PASS
TEST 174142 AFM FAIL STORE CHECK 3 (SINGLE X1)
TEST 174142 RESULT ----- 12:13:42 --> ***PASS
TEST 174143 AFM FAIL STORE CHECK 4 (SINGLE X2)
TEST 174143 RESULT ----- 12:13:43 --> ***PASS
TEST 174144 AFM FAIL STORE CHECK 5 (SINGLE X4)

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TEST 174144 RESULT ----- 12:13:43 --> ***PASS
TEST 174145 AFM FAIL STORE CHECK 6 (SINGLE X9)
TEST 174145 RESULT ----- 12:13:43 --> ***PASS
TEST 174146 AFM FAIL STORE CHECK 7 (SINGLE X18)
TEST 174146 RESULT ----- 12:13:44 --> ***PASS
TEST 174147 AFM FAIL STORE CHECK 8 (LSI LINK)
TEST 174147 RESULT ----- 12:13:44 --> ***PASS
TEST 174148 AFM FAIL STORE CHECK 9
TEST 174148 RESULT ----- 12:13:44 --> ***PASS
TEST 174149 AFM FAIL STORE CHECK 10
TEST 174149 RESULT ----- 12:13:45 --> ***PASS
TEST 174150 AFM PFC FAIL COUNT CHECK
TEST 174150 RESULT ----- 12:13:45 --> ***PASS
TEST 177000 PDS REGISTER READ/WRITE CHECK 1
TEST 177000 RESULT ----- 12:13:46 --> ***PASS
TEST 177010 PDS REGISTER READ/WRITE CHECK 2
TEST 177010 RESULT ----- 12:13:47 --> ***PASS
TEST 177100 PDS MEMORY READ/WRITE CHECK 1
TEST 177100 RESULT ----- 12:13:47 --> ***PASS
TEST 177110 PDS MEMORY READ/WRITE CHECK 2
TEST 177110 RESULT ----- 12:13:47 --> ***PASS
TEST 177200 PDS DATA SELECT CHECK 1
TEST 177200 RESULT ----- 12:15:30 --> ***PASS
TEST 177202 PDS DATA SELECT CHECK 3
TEST 177202 RESULT ----- 12:15:37 --> ***PASS
TEST 177210 PDS DATA INVERT CHECK
TEST 177210 RESULT ----- 12:15:37 --> ***PASS
TEST 177220 PDS ENABLE CHECK
TEST 177220 RESULT ----- 12:15:39 --> ***PASS
TEST 177300 PDS DRE/CPE PATTERN CONTROL CHECK
TEST 177300 RESULT ----- 12:18:26 --> ***PASS
TEST 177500 PDS PARALLEL REGISTER READ/WRITE CHECK 1
TEST 177500 RESULT ----- 12:18:31 --> ***PASS
TEST 177510 PDS PARALLEL REGISTER READ/WRITE CHECK 2
TEST 177510 RESULT ----- 12:18:36 --> ***PASS
TEST 177520 PDS PARALLEL REGISTER READ/WRITE CHECK 3
TEST 177520 RESULT ----- 12:19:08 --> ***PASS
TEST 177800 PDS SINGLE BIT MODE CHECK
TEST 177800 RESULT ----- 12:19:13 --> ***PASS
TEST 177810 PDS PATTERN CYCLE SHIFT CHECK
TEST 177810 RESULT ----- 12:20:44 --> ***PASS
TEST 177820 PDS DRE/CPE CYCLE SHIFT CHECK 1
TEST 177820 RESULT ----- 12:21:27 --> ***PASS
TEST 177830 PDS DRE/CPE CYCLE SHIFT CHECK 2
TEST 177830 RESULT ----- 12:21:30 --> ***PASS
TEST 177840 PDS PAT CONT PTMUX MODE CHECK
TEST 177840 RESULT ----- 12:21:31 --> ***PASS
TEST 177850 PDS PAT 2ND DRE/CPE CONT CHECK
TEST 177850 RESULT ----- 12:21:35 --> ***PASS
TEST 177860 PDS PAT 2ND SINGLE BIT MODE CHECK
TEST 177860 RESULT ----- 12:21:36 --> ***PASS
TEST 177870 PDS PAT 2ND CYCLE SHIFT CHECK
TEST 177870 RESULT ----- 12:21:40 --> ***PASS
TEST 177890 PDS DATA PARALLEL SERIAL CHECK
TEST 177890 RESULT ----- 12:21:41 --> ***PASS
TEST 177910 PDS RESOURCE DATA SWITCHING CHECK
TEST 177910 RESULT ----- 12:21:42 --> ***PASS
TEST 179000 FMUX CHANNEL SENSE CHECK
TEST 179000 RESULT ----- 12:21:43 --> ***PASS
TEST 179010 FMUX REGISTER READ/WRITE CHECK
TEST 179010 RESULT ----- 12:21:43 --> ***PASS
TEST 179020 FMUX DATA READ/WRITE CHECK
TEST 179020 RESULT ----- 12:21:43 --> ***PASS
TEST 179040 FMUX RATE CYCLE DELAY READ/WRITE CHECK
TEST 179040 RESULT ----- 12:22:04 --> ***PASS

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TEST 179200 FMUX DATA SELECT CHECK
TEST 179200 RESULT ----- 12:22:08 --> ***PASS
TEST 179210 FMUX DATA CONNECTION CHECK
TEST 179210 RESULT ----- 12:22:08 --> ***PASS
TEST 179500 FMUX PARALLEL DATA READ/WRITE CHECK
TEST 179500 RESULT ----- 12:22:09 --> ***PASS
TEST 179510 FMUX PARALLEL DATA READ/WRITE CHECK 2
TEST 179510 RESULT ----- 12:22:09 --> ***PASS
TEST 180050 MIXU I/F BOARD MOUNT CHECK
TEST 180050 RESULT ----- 12:22:09 --> ***PASS
TEST 180054 MIXU I/F PARALLEL REGISTER CHECK
TEST 180054 RESULT ----- 12:22:09 --> ***PASS
TEST 180056 MIXU I/F REF CLOCK STATUS CHECK
TEST 180056 RESULT ----- 12:22:09 --> ***PASS
TEST 180070 MIXU I/F PSRR REGISTER WRITE/READ CHECK
TEST 180070 RESULT ----- 12:22:09 --> ***PASS
TEST 180200 MIXU DCAP3 BOARD MOUNT CHECK
TEST 180200 RESULT ----- 12:22:09 --> ***PASS
TEST 180205 MIXU DCAP3 REGISTER WRITE/READ CHECK
TEST 180205 RESULT ----- 12:22:09 --> ***PASS
TEST 180206 MIXU DCAP3 REGISTER RESET CHECK
TEST 180206 RESULT ----- 12:22:09 --> ***PASS
TEST 180210 MIXU DCAP3 PCI REGISTER WRITE/READ CHECK
TEST 180210 RESULT ----- 12:22:09 --> ***PASS
TEST 180211 MIXU DCAP3 PCI REGISTER RESET CHECK
TEST 180211 RESULT ----- 12:22:09 --> ***PASS
TEST 180215 MIXU DCAP3 PCI MEMORY WRITE/READ CHECK
TEST 180215 RESULT ----- 12:24:18 --> ***PASS
TEST 180216 MIXU DCAP3 MEMORY CLEAR CHECK
TEST 180216 RESULT ----- 12:24:20 --> ***PASS
TEST 180220 MIXU DCAP3 FMUX LR LINE CHECK
TEST 180220 RESULT ----- 12:25:06 --> ***PASS
TEST 180221 MIXU DCAP3 FMUX HR LINE CHECK
TEST 180221 RESULT ----- 12:25:53 --> ***PASS
TEST 180230 MIXU DCAP3 RATE NEGLECT COUNTER CHECK
TEST 180230 RESULT ----- 12:26:00 --> ***PASS
TEST 180240 MIXU DCAP3 ENABLE CONTROL CHECK
TEST 180240 RESULT ----- 12:26:00 --> ***PASS
TEST 180241 MIXU DCAP3 STORE CONTROL CHECK
TEST 180241 RESULT ----- 12:26:00 --> ***PASS
TEST 180242 MIXU DCAP3 ASYNCHRONOUS DUT CONTROL CHECK
TEST 180242 RESULT ----- 12:26:01 --> ***PASS
TEST 180250 MIXU DCAP3 4-INPUT OR MODE CHECK
TEST 180250 RESULT ----- 12:26:04 --> ***PASS
TEST 180251 MIXU DCAP3 2-INPUT OR MODE CHECK
TEST 180251 RESULT ----- 12:26:07 --> ***PASS
TEST 180252 MIXU DCAP3 PARALLEL-INPUT MODE CHECK
TEST 180252 RESULT ----- 12:26:08 --> ***PASS
TEST 180260 MIXU DCAP3 32/16/8-BIT SERIAL-PARALLEL CONVERT CHECK
TEST 180260 RESULT ----- 12:26:51 --> ***PASS
TEST 180261 MIXU DCAP3 32-BIT SERIAL-PARALLEL CONVERT CHECK
TEST 180261 RESULT ----- 12:27:56 --> ***PASS
TEST 180270 MIXU DCAP3 ADDRESS COUNTER CHECK
TEST 180270 RESULT ----- 12:28:03 --> ***PASS
TEST 180271 MIXU DCAP3 ADDRESS COUNTER STATUS CHECK
TEST 180271 RESULT ----- 12:28:03 --> ***PASS
TEST 180272 MIXU DCAP3 DATA COUNTER (COUNTER MODE) CHECK
TEST 180272 RESULT ----- 12:28:07 --> ***PASS
TEST 180273 MIXU DCAP3 CAPTURE STATUS CHECK
TEST 180273 RESULT ----- 12:28:07 --> ***PASS
TEST 193128 DPU-PG CABLE CONNECTION CHECK
TEST 193128 RESULT ----- 12:28:08 --> ***PASS
* DIAGNOSIS END AT 2019/08/21 12:28:08 *
SYSTEM RESULT .....>> ***PASS
T6577 initialization data auto save start at 2019/08/21 12:28:08

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DPU initialization data successfully saved.
PMU initialization data successfully saved.
SQPG initialization data successfully saved.
FPU initialization data successfully saved.
TRIGFP initialization data successfully saved.
THU initialization data successfully saved.
TRIGTH initialization data successfully saved.
T6577 initialization data auto save finish at 2019/08/21 12:28:48
-----> PASS