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Nextest Magnum II PV Test System (JAD-38644)

Make:	Teradyne/ Nextest
Model:	Magnum II PV
Vintage:	2017
Quantity:	1
Inspection:	Available with PO

Configuration:

- **(2) SAB 511501**
- **(1) IPC firmware 5.4.14**

Includes:

- **Workstation: Dell T5810**
- **Manipulator: Esmo or SCM available**

Intel(R) Pentium(R) M processor 1.80GHz, 995MB total physical memory.
 Microsoft Windows XP
 2600.xpsp.080413-2111

- Revision Codes for Boards -

*** Summary of board set for SA pn: 511501 ***

*** Summary of option board set for HOB5 Assembly pn: 513182 ***

Board Name	Serial Number	PWB Number	PWB Rev	PWA Number	PWA Rev	Site 1 (Chassis 1, Slot 1)									
						LVM SDR/DDR	DBM GBits	ECRM GBits	ECRS GBits	RCM MBits	X bits	Y bits	D bits		
HSB2		508262	2	508263	12	64/128	(4) 18	(2) 36	(4) 36	(4) 36	(2) 24*	24*	36*		
PE32_1		511264	3	511265	7										
PE32_2		511264	3	511265	7										
PE32_3		511264	3	511265	7										
PE32_4		511264	3	511265	7										
DPX20_1		504582	9	508488	9										
DPX20_2		504582	9	508488	9										
DPX20_3		504582	9	508488	9										
DPX20_4		504582	9	508488	9										
IPC		509681	2	509680	20	Firmware Revision : 5.4.14									
HOB_5		509988	3	513747	5										
DPONV_1		512543	1	512545	17										
DPC_1		513555	2	513554	2										
HVI_1		513176	3	513175	5										
DPONV_2		512543	1	512545	17										
DPC_2		513555	2	513554	2										
HVI_2		513176	3	513175	5										
DPONV_3		512543	1	512545	17										
DPC_3		513555	2	513554	2										
HVI_3		513176	3	513175	5										
DPONV_4		512543	1	512545	17										
DPC_4		513555	2	513554	2										
HVI_4		513176	3	513175	5										
DPONV_5		512543	1	512545	17										
DPC_5		513555	2	513554	2										
HVI_5		513176	3	513175	5										
DPONV_6		512543	1	512545	17										
DPC_6		513555	2	513554	2										
HVI_6		513176	3	513175	5										
DPONV_7		512543	1	512545	17										
DPC_7		513555	2	513554	2										
HVI_7		513176	3	513175	5										
DPONV_8		512543	1	512545	17										
DPC_8		513555	2	513554	2										
HVI_8		513176	3	513175	5										

* NOTE: The ECR X, Y, D bits represent the maximum allowable configuration.

* NOTE: This tester is properly configured to support the 300mA/600mA DPS option.



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